

# Brain-inspired computing needs a master plan.

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## Preface

New computing technologies inspired by the brain promise fundamentally different ways to process information with extreme energy efficiency and the ability to handle the avalanche of unstructured and noisy data that we are generating at an ever-increasing rate. To realise this promise requires a brave and coordinated plan to bring together disparate research communities and to provide them with the funding, focus and support needed. We have done this in the past with digital technologies; we are in the process of doing it with quantum technologies; can we now do it for brain-inspired computing?

## Main

Modern computing systems consume far too much energy. They are not sustainable platforms for the complex Artificial Intelligence (AI) applications that are increasingly a part of our lives. We usually don't see this, particularly in the case of cloud-based systems, as we focus on functionality – how *fast* are they; how *accurate*; how many parallel operations per second? We are so accustomed to accessing information near-instantaneously that we neglect the energy, and therefore environmental, consequences of the computing systems giving us this access. Nevertheless, each Google search has a cost: data centres currently use around 200 terawatt hours of energy per year, forecast to grow by around an order of magnitude by 2030<sup>1</sup>. Similarly, the astonishing achievements of high-end AI systems such as DeepMind's AlphaGo and AlphaZero, which can beat human experts at complex strategy games, require thousands of parallel processing units, each of which can consume around 200 Watts<sup>2</sup>.

While not all data-intensive computing requires AI or Deep Learning (DL), DL is deployed so widely that we must worry about its environmental cost. We should also consider applications including the Internet of Things (IoT) and autonomous robotic agents that may not need always to be operated by computationally intense DL algorithms but must still reduce their energy consumption. The vision of the IoT cannot be achieved if the energy requirements of the myriad connected devices are too high. Recent analysis shows that increasing demand for computing power vastly outpaces improvements made through Moore's law scaling<sup>3</sup>. Computing power demands now double every two months (Figure 1a). Remarkable improvements have been made through a combination of smart architecture and software-hardware co-design. For example, the performance of NVIDIA GPUs has improved by the factor of 317 since 2012: far beyond what would be expected from Moore's law alone (Figure 1b) – although the power consumption of units has increased from ~25 W to around 320 W in the same period. Further impressive performance improvements have been demonstrated at the R&D stage (Figure 1b in red) and it is likely that we can achieve more [4,5]. Unfortunately, it is unlikely that conventional computing solutions alone will cope with demand over an

extended period. This is especially apparent when we consider the shockingly high cost of training required for the most complex DL models (Figure 1c). We need alternative approaches.

The energy problem is largely a consequence of digital computing systems storing data separately from where they are processed. This is the classical von Neumann architecture underpinning digital computing systems. Processors spend most of their time and energy moving data. Luckily, we can improve the situation by taking inspiration from biology, which takes a different approach entirely – co-locating memory and processing, encoding information in a wholly different way or operating directly on signals, and employing massive parallelism, for example (Box 1). There is a system that achieves both energy efficiency and advanced functionality remarkably well: the brain. Recognising that we still have much to learn about how the brain operates and that our aim is not simply to emulate biological systems, we can nevertheless learn from the significant progress in neuroscience and computational neuroscience in the last few decades. We know just enough about the brain to use it as an inspiration.

## Biological inspiration

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Biology does not separate data storage from processing. The same elements – principally neurons and synapses – perform both functions in massively parallel and adaptable structures. The  $10^{11}$  neurons and  $10^{15}$  synapses contained in the typical human brain expend approximately 20 W of power, while a digital simulation of an artificial neural network of approximately the same size consumes 7.9 MW<sup>6</sup>. That six order of magnitude gap poses us a challenge. The brain directly processes with extreme efficiency signals that are noisy. This contrasts with the signal to data conversion and high precision computing in our conventional computer system that produces huge costs in energy and time for even the most powerful digital supercomputers. Brain-inspired, or *neuromorphic*, computing systems could therefore transform the way we process signals and data, both in terms of energy efficiency and of their capacity to handle real-world uncertainty.

This is not a new idea. The term *neuromorphic*, describing devices and systems that mimic some functions of biological neural systems, was coined in the late 1980s by Carver Mead at the California Institute of Technology<sup>7,8</sup>. The inspiration came from work undertaken over previous decades to model the nervous system as equivalent electrical circuits<sup>9</sup> and to build *analogue* electronic devices and systems to provide similar functionality (Box 1).

A word about “data”. We use the term to describe information encoded in, say, an analogue signal or the physical response of a sensor, as well as the more standard computing-focused sense of digital data. When we refer to the brain “processing data” we describe an integrated set of signal processing tasks that do not rely on digitisation of signals in any conventional sense. We can think of brain-inspired systems operating at different levels: from analogue signal processing to working with large digital data sets. In the former case, we can avoid generating large data sets in the first place; in the latter we can greatly increase the efficiency of processing by moving away from the von Neumann model. Of course, there are good reasons why we represent data digitally for many applications: we want high precision, reliability and determinacy. However, digital abstraction discards massive amounts of information, found in the physics of transistors, for the minimum information quantum: a single bit. And we pay a significant energy cost by trading efficiency for reliability. As AI applications are often probabilistic at heart we must consider if this trade-off makes sense. The computational tasks underpinning AI applications are very compute-intensive (and therefore energy-hungry) when performed by conventional von Neumann computers. However, we might perform similar tasks much more energy-efficiently on analogue or mixed systems that

use a spike-based representation of information. There has therefore been a recent resurgence in interest in neuromorphic computing, driven by the growth in AI systems and by the emergence of new devices that offer new and exciting ways to mimic some of the capabilities of biological neural systems (Box 1).

Definitions of *neuromorphic* vary considerably. Loosely speaking, the story is a hardware one: neuromorphic chips aim to integrate and utilise various useful features of the brain, including in-memory computing, spike-based information processing, fine-grained parallelism, signal processing resilient to noise and stochasticity, adaptability, learning in hardware, asynchronous communication, and analogue processing. While it is debatable how many of these need to be implemented for something to be classified as neuromorphic, this is clearly a different approach from AI implemented on mainstream computing systems. Nevertheless, we should not be lost in terminology; the main question is whether this approach is *useful*.

Approaches to neuromorphic technologies lie on a spectrum between reverse-engineering the structure and function of the brain (analysis) and living with our current lack of knowledge of the brain but taking inspiration from what we do know (synthesis). Perhaps foremost among the former approaches is the Human Brain Project, a high-profile and hugely ambitious ten-year programme funded by the European Union from 2013. The programme supported the adoption and further development of two existing neuromorphic hardware platforms – SpiNNaker (at Manchester) and BrainScaleS (at Heidelberg) – as openly accessible neuromorphic platforms. Both systems implement highly complex silicon models of brain architectures to understand better the operation of the biological brain. At the other end of the spectrum numerous groups augment the performance of digital or analogue electronics using selected biologically-inspired methods. Figure 2 summarises the range of existing neuromorphic chips, divided into four categories depending on their position on the analysis-synthesis spectrum and their technology platform. It is important to remember that neuromorphic engineering isn't just about high-level cognitive systems, but also offering energy, speed and security gains (at least by removing the need for constant communication to the Cloud) in small-scale edge devices with limited cognitive abilities.

## Prospects

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We do not propose that neuromorphic systems will, or should, replace conventional computing platforms. Instead, precision calculations should remain the preserve of digital computation while neuromorphic systems can process unstructured data, perform image recognition, classification of noisy and uncertain data sets, and underpin novel learning and inference systems. In autonomous and IoT-connected systems, they can provide huge energy savings over their conventional counterparts. Quantum computing is also part of this vision. A practical quantum computer, while still several years away by any estimation, would certainly revolutionise many computing tasks. However, it is unlikely that IoT-connected smart sensors, edge computing devices, or autonomous robotic systems will adopt quantum computing without depending on cloud computing. There will remain a need for low-power computing elements capable of dealing with uncertain and noisy data. We can imagine a three-way synergy between digital, neuromorphic and quantum systems.

Just as the development of semiconductor microelectronics relied on many different disciplines, including solid state physics, electronic engineering, computer science, and materials science, neuromorphic computing is profoundly cross- and inter-disciplinary. Physicists, chemists, engineers, computer scientists, biologists, neuroscientists, all play key roles. Simply getting researchers from such a diverse set of disciplines to speak a common language is challenging. In our own work we spend considerable time and effort ensuring that

everyone in the room understands terminology and concepts in the same way. A case for bridging the communities of computer science (specifically AI) and neuroscience (initially computational neuroscience) is clear. After all, many concepts found in today's state-of-the-art AI systems arose in the 1970s and 80s in neuroscience though, of course, AI systems need not be completely bio-realistic. We must include other disciplines, recognising that many of the strides we have made in AI or neuroscience have been enabled by different communities – e.g. innovations in material science, nanotechnology, or electronic engineering. Further, conventional CMOS technology may not be the best fabric to efficiently implement new brain-inspired algorithms; innovations across the board are needed. Engaging these communities early reduces the risk of wasting effort on directions that have already been explored and failed, or of reinventing the wheel.

Further, we should not neglect the challenges of integrating new neuromorphic technologies at the system level. Beyond the development of brain-inspired devices and algorithms there are pressing questions around how existing, mainstream, AI systems can be replaced with functionally equivalent neuromorphic alternatives. This further emphasises the need for a fully integrated approach to brain-inspired computation.

We should point out that, despite the potential outlined above, there is as yet no compelling demonstration of a commercial neuromorphic technology. Existing systems and platforms are primarily research tools. However, this is equally true of quantum computing, which remains a longer-term prospect. We should not let this delay the development of brain-inspired computing; the need for lower power computing systems is pressing and we are tantalisingly close to achieving this with all the added functionality that comes from a radically different approach to computation. Commercial systems will surely emerge.

## Seizing the opportunity

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If neuromorphic computing is needed, how to achieve it? First, the technical requirements. Bringing together diverse research communities is necessary but not sufficient. Incentives, opportunities and infrastructure are needed. The neuromorphic community is a disparate one lacking the focus of quantum computing, or the clear roadmap of the semiconductor industry. Initiatives around the globe are starting to gather the required expertise, and early stage momentum is building. How can we build on this? Funding is key. Investment in neuromorphic research is nowhere near the scale of that in digital AI or quantum technologies (Box 2). While that is not surprising given the maturity of digital semiconductor technology, it is a missed opportunity. There are a few examples of medium-scale investment in neuromorphic R&D such as the IBM AI Hardware Centre's range of brain-inspired projects (including the TrueNorth chip), Intel's development of the Loihi processor, and the US Brain Initiative project, but the sums committed are well below what they should be given the promise of the technology to disrupt digital AI.

The neuromorphic community is a large and growing one, but one that lacks a focus. While there are numerous conferences, symposia and journals emerging in this space there remains much work to be done to bring the disparate communities together and to corral their efforts to persuade funding bodies and governments of the importance of this field.

The time is ripe for bold initiatives. At a national level, governments need to work with academic researchers and industry to create mission-oriented research centres to accelerate the development of neuromorphic technologies. This has worked well in areas such as quantum technologies and nanotechnology (the US National Nanotechnology Initiative demonstrates this very well<sup>10</sup>) and provides focus and stimulus. Such centres may be physical

or virtual but must bring together the best researchers across diverse fields. Their approach must be different from that of conventional electronic technologies in which every level of abstraction (materials, devices, circuits, systems, algorithms and applications) belongs to a different community. We need holistic and concurrent design across the whole stack. It isn't enough for circuit designers to consult computational neuroscientists before designing systems; engineers and neuroscientists must work together throughout the process to ensure as full an integration of bio-inspired principles into hardware as possible. Interdisciplinary co-creation must be at the heart of our approach. Research centres must house a broad constituency of researchers.

Alongside the required physical and financial infrastructure, we need a trained workforce. Electronic engineers are rarely exposed to ideas from neuroscience, and vice-versa. Circuit designers and physicists may have a passing knowledge of neurons and synapses but are unlikely to be familiar with cutting edge computational neuroscience. There is a strong case to set up Masters courses and doctoral training programmes to develop neuromorphic engineers. UK research councils sponsor Centres for Doctoral Training (CDTs) – focused programmes supporting areas with an identified need for trained researchers. CDTs can be single- or multi-institution; there are significant benefits to institutions collaborating on these programmes by creating complementary teams across institutional boundaries. Programmes generally work closely with industry and build cohorts of highly skilled researchers in ways that more traditional doctoral programmes often do not. There is a good case to be made to develop something similar, to stimulate interaction between nascent neuromorphic engineering communities and provide the next generation of researchers and research leaders. Pioneering examples include the Groningen Cognitive Systems and Materials research programme, which aims to train tens of doctoral students specifically in materials for cognitive (AI) systems<sup>11</sup>; the Masters programme in Neuroengineering at the Technical University of Munich<sup>12</sup>, ETH Zurich courses on analogue circuit design for neuromorphic engineering<sup>13</sup>; large-scale neural modelling at Stanford University<sup>14</sup>, and development of visual neuromorphic systems at the Instituto de Microelectrónica de Sevilla<sup>15</sup>. There is scope to do much more.

Similar approaches could work at the trans-national level. As always in research, collaboration is most successful when it is the best working with the best, irrespective of borders. In such an interdisciplinary endeavour as neuromorphic computing this is critical, so international research networks and projects undoubtedly have a role to play. Early examples include the European Neurotech consortium<sup>16</sup>, focusing on neuromorphic computing technologies, as well as the Chua Memristor Centre at the University of Dresden<sup>17</sup>, which brings together many of the leading memristor researchers across materials, devices and algorithms. Again, much more can and must be done.

How to make this attractive to governments? Government commitment to more energy-efficient bio-inspired computing can be part of a broader large-scale decarbonisation push. This will not only address climate change but also will accelerate the emergence of new, low-carbon, industries around big data, IoT, healthcare analytics, modelling for drug and vaccine discovery, and robotics, amongst others. If existing industries rely on ever more large-scale conventional digital data analysis, they increase their energy cost while offering sub-optimal performance. We can instead create a virtuous circle in which we greatly reduce the carbon footprint of the knowledge technologies that will drive the next generation of disruptive industries and, in doing so, seed a host of new neuromorphic industries.

If this sounds a tall order, consider quantum technologies. In the UK the government has so far committed around £1 billion to a range of quantum initiatives, largely under the umbrella of the National Quantum Technologies Programme. A series of research hubs, bringing together industry and academia, translate quantum science into technologies targeted at sensors and metrology, imaging, communications, and computing. A separate National Quantum

Computing Centre builds on the work of the hubs and other researchers to deliver demonstrator hardware and software to develop a general purpose quantum computer. China has established a multi-billion dollar Chinese National Laboratory for Quantum Information Sciences, while the USA in 2018 commissioned a National Strategic Overview for Quantum Information Science<sup>18</sup>, which resulted in a 5-year \$1.2 billion investment, on top of supporting a range of national quantum research centres<sup>19</sup>. Thanks to this research work there has been a global rush to start up quantum technology companies. One analysis found that in 2017 and 2018 funding for private companies reached \$450 million<sup>20</sup>. No such joined-up support exists for neuromorphic computing, despite the technology being more established than quantum, and despite its potential to disrupt existing AI technologies on a much shorter time horizon. Of the three strands of future computing in our vision, neuromorphic is woefully under-invested.

Finally, some words about what bearing the COVID-19 pandemic might have on our arguments. There is a growing consensus that the crisis has accelerated many developments already under way: for example, the move to more homeworking. While reducing commuting and travel has direct benefits – some estimates put the reduction in global CO<sub>2</sub> as a result of the crisis at up to 17%<sup>21</sup> – new ways of working have a cost. To what extent will carbon savings from reduced travel be offset by increased data centre emissions? If anything, the COVID pandemic further emphasises the need to develop low carbon computing technologies such as neuromorphic systems.

Our message about how to realise the potential of neuromorphic systems is clear: Provide targeted support for collaborative research through the establishment of research centres of excellence; provide agile funding mechanisms to enable rapid progress; provide mechanisms for close collaboration with industry to bring in commercial funding and generate new spin-outs and start-ups, similar to schemes already in place for quantum tech; develop training programmes for the next generation of neuromorphic researchers and entrepreneurs; and do all of this quickly and at scale.

Neuromorphic computing has the potential to transform our approach to AI. Thanks to the conjunction of new technologies and a massive, growing demand for efficient AI we have a timely opportunity. Bold thinking is needed, and bold initiatives to support this thinking. Will we seize the opportunity?

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### Author contributions

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Both authors contributed equally to the manuscript and revisions.

### Competing interests

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The authors declare the following competing interests:

The authors are founders and directors of Intrinsic Semiconductor Technologies Ltd ([www.intrinsicst.com](http://www.intrinsicst.com)), a spin-out company commercialising silicon oxide RRAM.



# Figures

Figure 1.

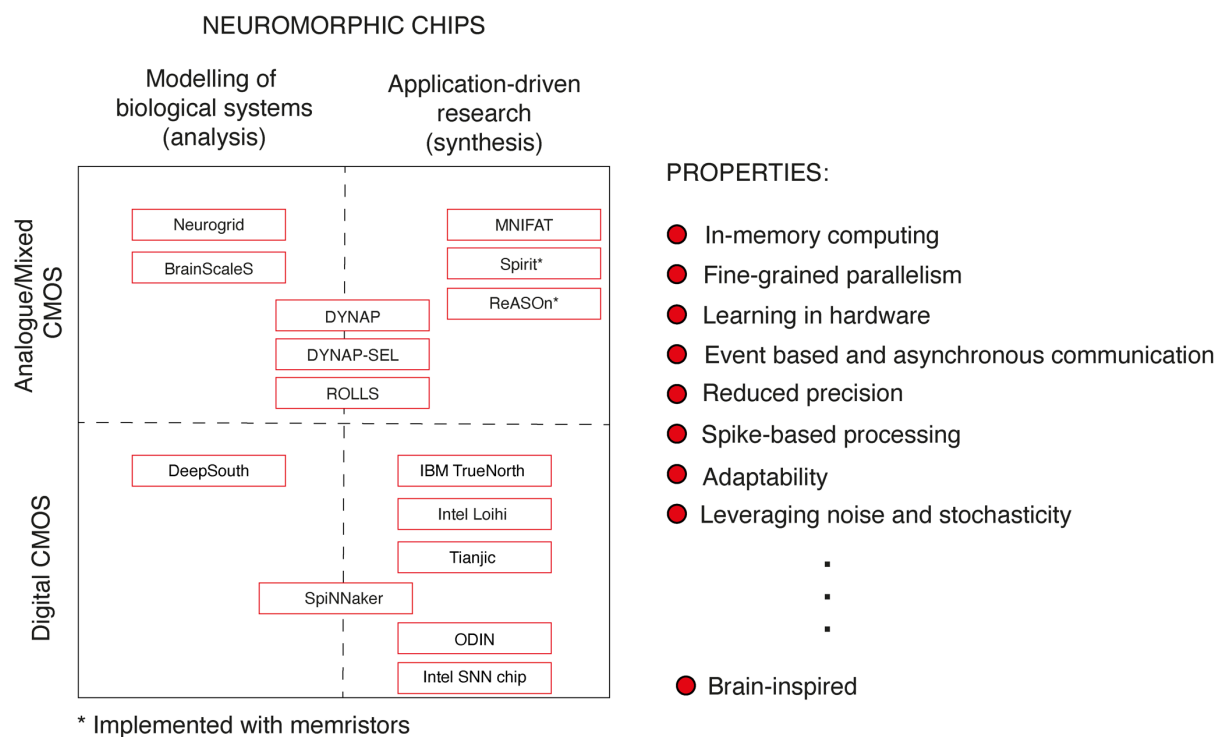


**Figure 1. Computational demands are increasing rapidly. (a)** The increase in computing power demands over the past four decades expressed in PetaFLOPS-days. Until 2012, computing power demand doubled every 24 months; recently this has shortened to approximately every two months. The colour legend indicates different application domains. Data taken from [3]. **(b)** Improvements in AI hardware efficiency over the last five years. State-

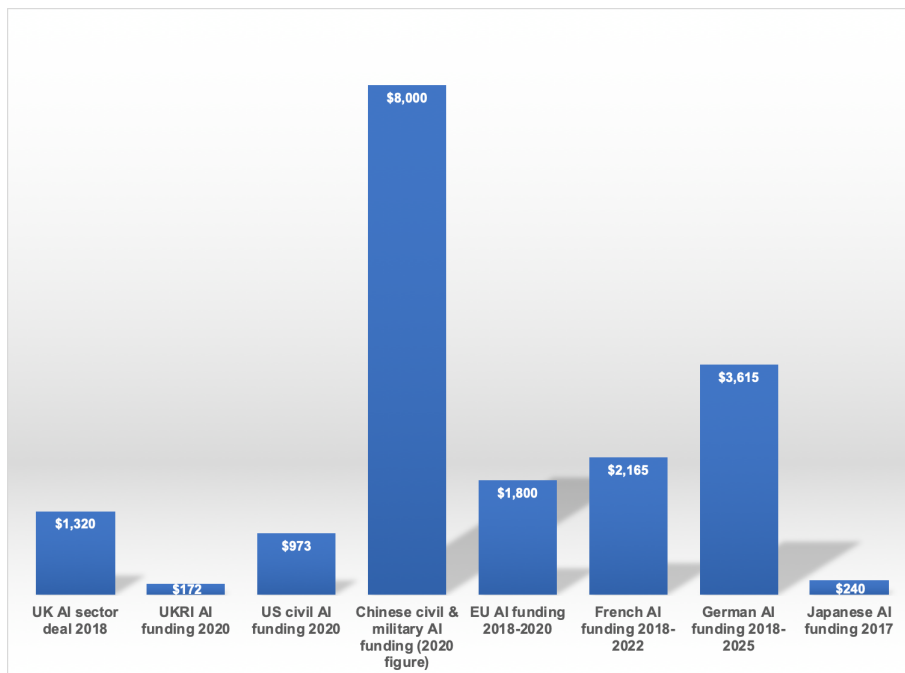


of-the-art solutions have driven increases in computing efficiency of over 300 times. Solutions in research and development promise further improvements. (c) Increase since 2011 of the costs of training AI models. Such an exponential increase is clearly unsustainable. Data taken from [22].

Figure 2.



**Figure 2. The landscape of neuromorphic systems.** Neuromorphic chips can be classified as either modelling biological systems or applying brain-inspired principles to novel computing applications. They may be further subdivided into those based on digital CMOS with novel architecture (for example, spikes may be simulated in the digital domain rather than implemented as analogue voltages) and those implemented using some degree of analogue circuitry. In all cases, however, they share at least some of the properties listed on the right-hand side, which distinguish them from conventional CMOS chips. Here we classify examples of recently developed neuromorphic chips. Further details of each can be found in the relevant reference: Neurogrid<sup>23</sup>, BrainScaleS<sup>24</sup>, MNIFAT<sup>25</sup>, DYNAP<sup>26</sup>, DYNAP-SEL<sup>27</sup>, ROLLS<sup>28</sup>, Spirit<sup>29</sup>, ReASOn<sup>30</sup>, DeepSouth<sup>31</sup>, SpiNNaker<sup>32</sup>, IBM TrueNorth<sup>33</sup>, Intel Loihi<sup>34</sup>, Tianjic<sup>35</sup>, ODIN<sup>36</sup>, and the Intel SNN chip<sup>37</sup>.



**Box 2 figure: Public investment in AI.** A comparison of recent global public research funding of digital AI technologies. Figures are in US dollar equivalent (2021 exchange rate) and are expressed as millions of dollars. While some are in-year snapshots (eg UKRI funding committed for 2020), some have no specified period (the UK AI sector deal, for example), and others are for multi-year programmes, the figure illustrates the scale of public funding in digital technologies. Disruption of the AI ecosystem by the development of efficient neuromorphic technologies would put much of this investment at risk.

## BOX 1 – What do we mean by “neuromorphic” systems?

Taking inspiration from the brain allows us to approach information processing fundamentally differently to the way existing conventional computing systems work. Different brain-inspired (“neuromorphic”) platforms use combinations of different approaches: analogue data processing, asynchronous communication, massively parallel information processing or spiking-based information representation. These properties distinguish them from von Neumann computers.

The term *neuromorphic* encompasses at least three broad communities of researchers, distinguished by whether their aim is to emulate neural function (i.e. reverse-engineer the brain), simulate neural networks (i.e. develop new computational approaches), or engineer new classes of electronic device.

*Neuromorphic engineering* looks at how the brain uses the physics of biological synapses and neurons to “compute”. Neuromorphic engineers work to emulate the functions of biological neurons and synapses by harnessing the physics of analogue electronics – such as carrier tunnelling, charge retention on silicon floating gates, and the exponential dependence of various device or material properties on field – to define elementary operations to underpin audio or video processing or smart sensors, for example. Transistors are used as analogue circuit elements with rich dynamic behaviour rather than binary switches. More details can be found in [38] and related work.

*Neuromorphic computing* looks to biology to inspire new ways to process data. This could be considered as the computing science of neuromorphic systems. Research looks to simulate the structure and/or operation of biological neural networks., which may mean co-locating storage and computing, as the brain does; or perhaps adopting wholly different ways of computing based on voltage spikes modelling the action potentials of biological systems.

Underpinning everything are the devices and materials needed to implement bio-inspired functions. Here, recent developments promise new electronic and photonic devices whose properties we can tailor to mimic biological elements such as synapses and neurons. These *neuromorphic devices* could provide exciting new technologies to expand the capabilities of neuromorphic engineering and computing.

Foremost amongst these new devices are *memristors*: electronic devices whose resistance is a function of their history. Their complex dynamic electrical response means they can be used as digital memory elements, as variable weights in artificial synapses, as cognitive processing elements, optical sensors, and devices that mimic biological neurons<sup>39</sup>. They may embody some of the functionality of biological dendrites<sup>40</sup> and their dynamic response can generate oscillatory behaviour similar to that of the brain – controversially, operating on the edge of chaos<sup>41,42</sup>. They may also be linked with biological neurons in a single system<sup>43</sup>. They do all of this while expending very little energy.

## BOX 2 – The AI funding landscape

Investment in “conventional” digital AI is booming, fuelled by the need to process ever-increasing volumes of data, and the development of hardware to support existing compute- and memory-intensive algorithms. The UK government announced in April 2018 a £950 million “sector deal” in digital AI, in addition to existing research council support. France announced a €1.8 billion government investment in AI from 2018 to 2022<sup>44</sup>, Germany committed €3 billion from 2018 to 2025, while Japan spent ¥26 trillion in 2017. US government funding of civil AI technologies was \$973 million in 2020<sup>45</sup>; figures are harder to come by for US military AI funding, as non-AI projects are often included in published analysis. China is estimated to be investing up to \$8 billion in both civil and military AI and is constructing a \$2.1 billion AI research park near Beijing<sup>46</sup>, while the European Commission committed €1.5 billion in the period 2018-2020<sup>47</sup>. Commercial investment dwarfs this. In the USA one estimate puts the total investment in AI companies in 2019 at \$19.5 billion<sup>48</sup>, and global investment is predicted to be around \$98 billion by 2023<sup>49</sup>. Such sums must be considered at risk if our current hardware systems cannot support potentially disruptive neuromorphic algorithms and architectures. If neuromorphic technologies offer anything like the efficiency savings and enhanced performance they promise, smart money will hedge its bets on novel technologies and architectures alongside digital systems.

Comparable figures are not available for neuromorphic technologies, as they currently lack focus and government-level visibility. Research funding is therefore piecemeal and at project, rather than strategic, level. While there have been various estimates published – for example, that the global neuromorphic chip market will grow from \$111 million in 2019 to \$366 million in 2025<sup>50</sup>, the safest conclusion to draw is that funding of neuromorphic systems lags way behind that of digital AI or of quantum (of which more below).

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