Electron Trapping Mechanism in LaAlO$_3$/SrTiO$_3$ Heterostructures

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In LaAlO$_3$/SrTiO$_3$ heterostructures, a still poorly understood phenomenon is that of electron trapping in back-gating experiments. Here, by combining magnetotransport measurements and self-consistent Schrödinger-Poisson calculations, we obtain an empirical relation between the amount of trapped electrons and the gate voltage. The amount of trapped electrons decays exponentially away from the interface. However, contrary to earlier observations, we find that the Fermi level remains well within the quantum well. The enhanced trapping of electrons induced by the gate voltage can therefore not be explained by a thermal escape mechanism. Further gate sweeping experiments strengthen that conclusion. We propose a new mechanism which involves the electromigration and clustering of oxygen vacancies in SrTiO$_3$ and argue that such electron trapping is a universal phenomenon in SrTiO$_3$-based two-dimensional electron systems.

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Controlling the electronic properties of materials by applying an external voltage is at the heart of modern electronics. This is also true for oxide heterostructures, where the quasi-two-dimensional electron gas (Q2DEG) discovered at the interface between LaAlO$_3$ (LAO) and SrTiO$_3$ (STO) [1] displays a multitude of physical properties, such as superconductivity [2], signatures of magnetism [3–5] and even their coexistence [6,7]. Due to the large permittivity of the STO substrate [8], the carrier density and mobility of the Q2DEG can be modulated by a back-gate voltage ($V_G$). Gate-tunable insulator to metal transitions [9], insulator to superconductor transitions [10] and Rashba spin-orbit interactions [11,12] have been reported. At the LAO/STO interface, the Q2DEG is confined in a quantum well (QW) on the STO side and the band structure is formed by the Ti $t_{2g}$ orbitals. For LAO films grown on STO (001) substrates, the $d_{xy}$ band lies below the $d_{xz,yz}$ bands in energy [13–15]. Applying $V_G$ across the STO substrate changes the carrier density in the QW. A Lifshitz transition occurs when the Fermi level is tuned across the bottom of the $d_{xz,yz}$ bands [16]. In back-gating experiments, a commonly observed phenomenon is that the sheet resistance ($R_s$) follows an irreversible route when $V_G$ is swept first forward and then backward [10,17–20]. The explanation given by Biscaras et al. [18] is that the Fermi level lies intrinsically close to the top of the QW. High-mobility electrons escape and get trapped in STO when the carrier density is beyond a threshold. But the relations between the amount of trapped electrons, their spatial distribution, and the gate voltage are still unknown. We study these relations by combining magnetotransport measurements and self-consistent Schrödinger-Poisson calculations on samples grown by sputtering and pulsed laser deposition (PLD). In both cases, the thermal escape mechanism cannot be reconciled with our results. Further gate sweeping experiments strengthen this conclusion. We propose a new mechanism which involves the electromigration and clustering of oxygen vacancies in SrTiO$_3$. Results of the sputtered sample are discussed in the main text, those of the PLD-grown sample are in the Supplemental Material [21].

Hall bar devices were used as depicted in Fig. 1(a) (inset), of length 1000 μm and width 150 μm. First, a sputtered amorphous AlO$_x$ hard mask in the form of a negative Hall bar geometry (thickness ~15 nm) was fabricated on a TiO$_2$-terminated STO (001) substrate [26] by photolithography. Then, 15 unit cells of LAO film were deposited at 800 °C in an Ar pressure of 0.04 mbar by 90° off-axis sputtering [27]. Finally, the sample was in situ annealed at 600 °C in 1 mbar of oxygen for 1 h. The backgate electrode was formed by uniformly applying a thin silver paint layer (Ted Pella, Inc.) on the back of the substrate. Device fabrication details are described in the Supplemental Material [21]. The longitudinal and transverse resistances ($R_{xx}$, $R_{xy}$) were measured simultaneously by standard lock-in technique ($f = 13.53$ Hz and $\text{rms} = 1.0$ μA). Magnetotransport measurements were performed under different $V_G$ at 4.2 K in a superconducting magnet (field sweep ±15 T). The maximum applied $V_G$ was ±200 V. The leakage current was less than 1 nA during the measurement.
The device was cooled down to 4.2 K with \( V_G \) grounded. Figure 1(a) shows the \( V_G \) dependence of the sheet resistance \( R_s \), which is consistent with earlier reports [19]. After backward sweep (BS) led to a metal-insulator transition above the virgin curve when \( V_G \) was swept forward again, \( R_s \) increased faster in BS than in BS, because \( \Delta n \) increased faster in BS than in BS, which is close to earlier reported values [16]. In BS, \( n_{\text{tot}} \) decreased to 1.33 × 10^{13} \text{ cm}^{-2} at 10 V, which is the onset of the MIT, comparable to the earlier reported carrier density \( (n_G) \) of 0.5–1.5 × 10^{13} \text{ cm}^{-2} for the MIT [28]. In BS, the carrier densities of the bands were tuned reversibly as in BS and the system was fully recovered when 50 V was reapplied. In FSrev, \( n_{\text{tot}} \) saturated at 2.17 × 10^{13} \text{ cm}^{-2} beyond 125 V. In BS, the MIT occurred at 110 V with a carrier density of 1.25 × 10^{13} \text{ cm}^{-2}, which could be due to the Hall bar contacts becoming insulating faster than the channel [29]. A noteworthy feature is that the amount of gate-induced trapped electrons is independent of the number of backward sweeps and is only related to \( V_G \).

First, we study the relation between the amount of trapped electrons and the gate voltage. In a backgating experiment, the total amount of electrons \( (n^G_{\text{tot}}) \) induced by \( V_G \), as shown by the purple curves in Figs. 2(a) and 2(b),...
can be calculated using a parallel plate capacitor model [30,31]:

\[ n_G^{tr}(V_G) = \int_{V_1}^{V_2} \frac{e_0}{e d_{STO}} e_r(V_G) dV_G, \] (1)

where \( e_0 \) is the vacuum permittivity, \( e \) is the electron charge and \( d_{STO} \) is the thickness of the STO substrate (0.5 mm). The field-dependent permittivity of the STO substrate \( e_r(V_G) \) is calculated following Ref. [32]:

\[ e_r(E) = 1 + \frac{B}{1 + (E/E_0)^2}^{1/3}, \] (2)

where the electric field \( E = V_G/d_{STO} \), \( B = 2.55 \times 10^4 \) and \( E_0 = 8.22 \times 10^5 \) V/m. In FS_{irrev} regimes, as shown in Fig. 2(a), a part of \( n_G^{tr} \) becomes gate-induced trapped electrons (\( n_G^{tr} \)) in STO. Subtracting \( n_G^{tr} \) from \( n_G^n \) will give the amount of gate-induced mobile electrons (\( n_G^m \)) which are doped into the QW. We find that the relation between \( n_G^{tr} \) and \( V_G \) can be described using the following expression:

\[ n_G^{tr}(V_G) = N(1-e^{-V_G/400}), \] (3)

which yields the yellow curve, where \( N = 6.2 \times 10^{13} \) cm\(^{-2}\). The subtraction (\( n_G^{tr} - n_G^n = n_G^m \)) is given by the green curve, and gives a good description of the measured \( n_G^n \) (open black circle). In BS regimes, as shown in Fig. 2(b), \( n_G^{tr} \) is given by \( V_G \) according to Eq. (1). However, the value of \( n_G^{tr} \) is fixed at the \( n_G^n(V_G^{max}) \). Thus, \( n_G^{tr} \) is smaller than its counterpart in FS_{irrev} regimes. In both BS regimes the calculated \( n_G^n \) is in good agreement with the experimental data. Moreover, due to the field-dependent permittivity, \( dn_G^{tr}/dV_G \) is decreasing as \( V_G \) increases. As a consequence, the same negative \( \Delta V_G \) removes more mobile electrons at 50 V rather than at 200 V, which could explain the fact that \( R_s \) increases faster in BS1 than in BS2. It should be noted that the empirical formula of \( n_G^{tr}(V_G) \) is not universal, but instead varies among samples. We performed similar \( V_G \) sweeps on two reference samples and observed slightly different \( V_G \) dependence of \( R_s \) (see Fig. S5 in the Supplemental Material [21]). Thus, \( n_G^{tr}(V_G) \) should always be obtained from experimental results.

Next, we use the self-consistent Schrödinger-Poisson (S-P) model to study the charge distribution and band occupation [15,33–36]. S-P calculations are based on the effective mass and envelope wave function approximations. Due to the orbital orientation, \( d_{xy} \) and \( d_{xz;yz} \) orbitals are heavy and light in the \( z \) direction, respectively. We take the effective masses as \( m_{x}^{*} = 14 m_e \) and \( m_{xz;yz}^{*} = 0.7 m_e \) [14,15,35], where \( m_e \) is the electron mass. We take \( z > 0 \) to be STO. In the original state, there are initial mobile electrons (\( n_G^m, 1.41 \times 10^{13} \) cm\(^{-2}\) in our sample) and initial trapped electrons (\( n_G^n \)) on the STO side, and an equivalent amount of positive charges on the LAO side to keep overall charge neutrality.

The spatial distributions of the trapped electrons, both \( n_G^{tr} \) and \( n_G^n(V_G) \), are input parameters of the S-P model, which effectively influence the \( V_G \) dependent occupation of the \( d_{xy} \) and \( d_{xz;yz} \) bands. In our calculations, we obtain the best results by using the following distribution of the trapped electrons:

\[ n_G^{tr}(z,V_G) = \begin{cases} 0 & \text{for } z < 0 \\ \frac{n_G^{tr}(V_G)}{\lambda} e^{-V_G/\lambda} & \text{for } z \geq 0 \end{cases} \] (4)

where \( n_G^{tr} = 6.4 \times 10^{13} \) cm\(^{-2}\) and \( \lambda = 50 \) nm. The integration range is from 0 to 100 nm, which is divided into 2000 equal sections. The calculated evolutions of \( n_{xy} \) and \( n_{xz;yz} \) in FS_{irrev} and BS regimes are shown in Figs. 2(c) and 2(d), closely agreeing with the experimental data.

Based on the above analysis, we could obtain the confining potential profile, the Fermi energy and the spatial distribution of mobile electrons occupying the \( d_{xy} \) and \( d_{xz;yz} \) bands. Figures 3(a)-3(c) show the results at 0, 50, and 200 V, respectively. The mobile electrons are confined within ~10 nm at the interface, which agrees with the reported spatial distribution of the Q2DEG [37–39]. Figure 3(d) shows the confining potential in a larger range. In all cases the Fermi level is well below the top of the QW; therefore the probability of mobile electrons thermally escaping [\( k_BT(4.2 \) K) \( \approx 0.36 \) meV] from the QW should be very low. The subband dispersions of the three cases are shown in Figs. 3(e)-3(g). We note that increasing \( V_G \) decreases the spacing between the subband levels.

In order to check the thermal escape mechanism [18] in more detail, we warmed the device to room temperature to remove the trapping [17], cooled down again to 4.2 K and performed multiple backward sweeps from 10 to 50 V. As shown in Fig. 4, a growing \( R_s \) separation between FS_{irrev} and BS is seen as \( V_G^{max} \) increases. In the thermal escape mechanism, electron trapping only occurs after \( R_s \) (or \( n_{tot} \)) reaches its saturation. However, our experiment clearly shows that trapping occurs immediately when positive \( V_G \) is applied and the amount of trapped electrons increases as \( V_G^{max} \) increases. So we can rule out thermal escaping of mobile electrons to be the mechanism for electron trapping. We performed the same magnetotransport measurements and S-P calculations on a PLD-grown sample. Although the characteristic transport and fitting parameters of the PLD sample were very different from the sputtered one, the Fermi level was also found to stay well within the quantum well.

Moreover, similar irreversible behavior has been reported in other Q2DEG systems, such as LaTiO\(_3\)/STO [18], LaVO\(_3\)/STO [20], (LaAlO\(_3\))\(_{0.3}\)(Sr\(_2\)AlTaO\(_6\))\(_{0.7}\)/STO [40], and amorphous LAO/STO [41]. Therefore the electron trapping phenomenon appears intrinsic to the STO substrate. We also checked whether the structural
phase transition of the STO substrate at 105 K plays a role in the trapping [42–44]. Gating experiments at 4, 80, and 120 K all showed hysteresis, as shown in the Supplemental Material [21]. We conclude that tetragonal domain formation is not important for what we observe.

We propose a two-step trapping mechanism involving redistribution of oxygen vacancies ($V_0$’s) in STO under influence of an electric field. The first step is the electromigration of $V_0$’s. Among all types of defects in STO, $V_0$ has the lowest activation enthalpy for migration [45] as reported in previous works [46–48]. The second step is the clustering of $V_0$’s which could form in-gap trapping states [49,50], of which the energy was recently determined to be $\sim 0.31$ and $\sim 1.11$ eV below the conduction band [51]. Figure 5 shows the dynamic resistance change during and after $V_G$ sweeps in the FS$^{rev}$ and BS regimes. The electron trapping mechanism can then be explained as follows. In FS$^{rev}$ regimes as shown in Fig. 5(a), the effect of

![FIG. 3. (a)–(c) S-P calculations confining potential profile (solid line), Fermi energy (dotted line), and spatial distribution of mobile electrons occupying $d_{xy}$ (red) and $d_{xz,yz}$ (blue) bands at (a) 0, (b) 50, and (c) 200 V. (d) Confining potential and Fermi energy in a larger range. (e), (f) S-P calculated subband dispersions in parabolic approximation at (e) 0, (f) 50, and (g) 200 V.](image)

![FIG. 4. $V_G$ dependence of $R_s$ at 4.2 K. The sweep order is similar to that in Fig. 1(a). Backward sweeps were performed from 10 to 50 V. Note that BS and FS$^{rev}$ overlap perfectly.](image)

![FIG. 5. (a), (b) Dynamic change of $R_s$ during $V_G$ sweeps in (a) FS$^{rev}$ and (b) BS regimes. $V_G$ was swept at a rate of 0.1 V/s. $R_s$ measurements kept on going for several minutes after the stabilization of $V_G$. (c), (d) Dynamic change of $R_s$ after $V_G$ sweeps in (c) FS$^{rev}$ and (d) BS regimes.](image)
increasing $V_G$ is twofold. One is to add electrons into the QW. The other is to push positively charged $V_O$’s migrating toward the interface. The clustering of the accumulated $V_O$’s then forms in-gap trapping states. Several sudden resistance jumps can be clearly seen during $V_G$ sweeps, which might be due to the formation of big $V_O$ clusters. Moreover, after stabilizing the gate voltage as shown in Figs. 5(b) and 5(d), sweeping $V_G$ only changes the carrier density in the QW without modifying the defect landscape near the interface. Therefore the system can be tuned in a reversible manner. A schematic illustration of our proposed electric field-driven trapping mechanism is shown in the Supplemental Material [21].

Summarizing, we studied electron trapping in LAO/STO heterostructures under backgate voltages. Combined magnetotransport measurements and self-consistent Schrödinger-Poisson calculations yield a relation between the amount of trapped electrons and the gate voltage as well as the spatial distribution of the trapped electrons. We propose a new trapping mechanism which involves the electromigration and clustering of oxygen vacancies in STO, since our analysis shows that the thermal escape mechanism is not valid. This is relevant for theoretical works [35,52,53], where the assumption was that all the gate-induced electrons land in the QW. We obtained qualitatively similar results from the samples grown by sputtering and PLD, and conclude that electron trapping is a universal phenomenon in SrTiO$_3$-based two-dimensional electron systems.

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