

Reduced Switching Connectivity for Large Scale Antenna Selection

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Abstract—In this paper, we explore reduced-connectivity radio frequency (RF) switching networks for reducing the analog hardware complexity and switching power losses in antenna selection (AS) systems. In particular, we analyze different hardware architectures for implementing the RF switching matrices required in AS designs with a reduced number of RF chains. We explicitly show that fully-flexible switching matrices, which facilitate the selection of any possible subset of antennas and attain the maximum theoretical sum rates of AS, present numerous drawbacks such as the introduction of significant insertion losses, particularly pronounced in massive multiple-input multiple-output (MIMO) systems. Since these disadvantages make fully-flexible switching suboptimal in the energy efficiency sense, we further consider partially-connected switching networks as an alternative switching architecture with reduced hardware complexity, which we characterize in this work. In this context, we also analyze the impact of reduced switching connectivity on the analog hardware and digital signal processing of AS schemes that rely on received signal power information. Overall, the analytical and simulation results shown in this paper demonstrate that partially-connected switching maximizes the energy efficiency of massive MIMO systems for a reduced number of RF chains, while fully-flexible switching offers sub-optimal energy efficiency benefits due to its significant switching power losses.

Index Terms—Antenna selection, massive MIMO, RF switching matrices, insertion losses, energy efficiency.

I. INTRODUCTION

THE constant growth in the number of mobile devices as well as the development of data-hungry applications have driven the design of novel wireless communications solutions. In this context, systems incorporating a large number of antennas at the base stations (BSs) and occupying larger bandwidths are leading candidates for 5G [1], [2]. In particular, massive MIMO for conventional microwave frequencies and

millimeter waves implement a large number of antennas for achieving unprecedented spatial resolutions and for compensating for the larger signal attenuations introduced at high frequencies [3], [4]. Simultaneously, these solutions motivate the design of novel digital and analog signal processing strategies, since the economic cost, power consumption, and hardware and digital signal processing complexities of systems with a dedicated RF chain per antenna might become burdensome [4]–[6]. In order to overcome the above challenges, a number of solutions such as hybrid analog-digital precoding schemes [4] and spatial modulation [7], [8] have been proposed. In this context, AS has also been posed as a feasible alternative for reducing the complexity in both small and large scale MIMO systems operating below 6 GHz [9]–[11], and comprises the focus of this work.

The characterization and development of AS systems have constituted the focus of several works, which study different features such as the acquisition of channel state information (CSI) [12], [13], their energy efficiency improvements [14], [15], or their practical implementation aspects [16], [17]. The implementation of AS as a means of reducing the excessive number of RF chains in massive MIMO has also attracted considerable attention [15], [18]–[22]. Particularly relevant in this context are the results presented in [21], [22], which illustrate that a large portion of the full-RF massive MIMO rates can be attained via AS in realistic propagation scenarios. However, none of the above-mentioned works presents a comprehensive analysis of a critical aspect: the design of the RF switching matrix that implements the AS.

The RF switching matrix represents the hardware components required in AS for interconnecting the RF chains with their selected antennas in implementations with a reduced number of RF chains [10], [16]. The design of RF switching matrices has been mostly studied in the area of microwave circuit implementation. Illustrative examples in this line include [23]–[28], where the focus is on fully-flexible switching matrices that facilitate the inter-connection of any input port to all output ports. The colossal growth of antenna numbers in massive MIMO also increases the complexity of RF switching, hence making these components a significant performance factor. In spite of acknowledging the crucial importance of this component [10], the impact of the switching network on the ergodic sum rates and energy efficiency of AS has been commonly ignored in the related literature. Only recently, the large insertion losses and complexity of switching matrices in massive MIMO have been considered in [22] and [29]. Specifically, [29] considers a switching

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network that only connects each RF chain to predefined subsets of antennas for reducing the complexity of switching between the large number of antennas required at millimeter wave frequencies. Similarly, [22] proposes to implement the switching matrix via binary switches to alleviate the insertion losses. However, this comes at the expense of reducing the input-output connectivity of the resultant switching architecture, which is partially-connected. Still, these works do not perform a thorough and detailed analysis of the hardware implications behind their designs and only focus on specific implementations such as binary switching matrices.

Considering the above, in this contribution we generalize the above-mentioned works to arbitrary switching architectures and concentrate on providing a detailed analysis of switching networks to characterize their influence in AS systems. In particular, we present a number of specific hardware implementations of switching matrices that are optimized under different criteria such as the number of internal connections or the signal power losses. In this context, we accurately characterize their insertion losses, which are shown to be critical for massive MIMO due to the large number of inputs (RF chains) and outputs (antennas) required. In contrast with [23]–[28], we also consider architectures with limited connectivity as a means for reducing the complexity of the fully-flexible switching networks conventionally considered. In this line, we also determine the ergodic sum capacity loss introduced due to the limited connectivity for received signal power-based AS systems, since power-based AS can offer a superior performance when practical CSI acquisition procedures are considered. Altogether, the results and generalized designs considered in this work provide a comprehensive view of the impact of RF switching matrices on the maximum sum achievable rates, hardware architecture and energy efficiency of AS systems.

The rest of the paper is structured as follows. Sec. II reviews the fundamentals of AS systems, including their CSI acquisition procedure. Sec. III and Sec. IV characterize the operation of fully-flexible and partially-connected switching matrices, respectively. Sec. V provides theoretical approximations of the ergodic sum capacity achieved by partially-connected AS systems. Sec. VI introduces the energy efficiency metric employed in Sec. VII, where numerical results are presented. The conclusions of this paper are finally drawn in Sec. VIII.

II. PRELIMINARIES

A. Downlink System Model

Let us consider a time-division duplex (TDD) multi-user MIMO (MU-MIMO) system comprised of a BS with N transmit antennas and K single-antenna mobile stations (MSs). The BS incorporates $M \in \{K, K+1, \dots, N\}$ RF chains to convey K independent data symbols to the MSs via AS. The baseband representation of the composite signal $\mathbf{y} \in \mathbb{C}^{K \times 1}$ received by the MSs can be expressed as

$$\mathbf{y} = \sqrt{\rho} \mathbf{H}_{[\mathcal{M}]} \mathbf{x} + \mathbf{w}, \quad (1)$$

where $\mathbf{H} \in \mathbb{C}^{K \times N}$ denotes the channel response between the BS antennas and the users, $\mathbf{x} \in \mathbb{C}^{M \times 1}$ is the transmit signal

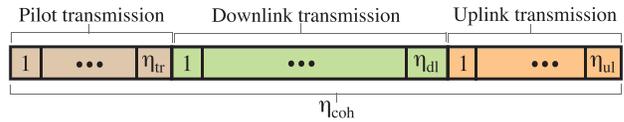


Fig. 1. Operation of a TDD communication system.

and $\mathbf{w} \in \mathbb{C}^{K \times 1} \sim \mathcal{CN}(\mathbf{0}, \mathbf{I}_K)$. In the previous expressions, \sim means “distributed as,” \mathbf{I}_K is the $K \times K$ identity matrix and $\mathcal{CN}(\mathbf{a}, \mathbf{A})$ represents a complex normally distributed vector with means \mathbf{a} and covariance matrix \mathbf{A} . Moreover, $\mathbf{H}_{[\mathcal{M}]} \in \mathbb{C}^{K \times M}$ is a submatrix of \mathbf{H} built by selecting the columns specified by the set $\mathcal{M} \subseteq \{1, \dots, N\}$ with cardinality $|\mathcal{M}| = M$. Here, $\mathcal{A} \subseteq \mathcal{B}$ indicates that \mathcal{A} is a subset of \mathcal{B} . We let $\mathbb{E}[\mathbf{x}^H \mathbf{x}] = K$, where $\mathbb{E}[\cdot]$ denotes statistical expectation. Based on the above, ρ represents the average transmission power per MS.

The sum capacity C of the MU-MIMO downlink system described in (1) is given by [30], [31]

$$C = \max_{\mathbf{P}} \left(\frac{\eta_{dl}}{\eta_{coh}} \right) \log_2 \det \left(\mathbf{I}_K + \rho \mathbf{P} \mathbf{H}_{[\mathcal{M}]} (\mathbf{H}_{[\mathcal{M}]})^H \right), \quad (2)$$

where $(\cdot)^H$ denotes the Hermitian transpose of a matrix, whereas η_{coh} and η_{dl} denote the total and downlink time-frequency symbols per channel coherence interval, respectively, as shown in Fig. 1. Moreover, $\mathbf{P} \in \mathbb{R}^{K \times K}$ is a diagonal user power allocation matrix satisfying $\sum_{i=1}^K \mathbf{P}_{i,i} = K$, where $\mathbf{A}_{i,j}$ denotes the i, j -th entry of \mathbf{A} .

B. Channel State Information Acquisition for AS Systems

The sum capacity C in (2) can only be achieved provided that full knowledge of the channel response \mathbf{H} is available at the BS [21], [32]. However, CSI acquisition poses a major challenge in AS systems with reduced RF chains, which constitutes the focus of this section. In order to characterize this issue we adopt a standard block fading model in the following, i.e., the channel is considered to remain approximately constant throughout a block comprised of η_{coh} time-frequency symbols, and to vary independently between blocks [33].

1) *Instantaneous CSI Acquisition for Reduced-RF Systems* ($M < N$): The AS systems considered in this paper only implement $M < N$ RF chains. This constraint entails that only M signals from the antenna ports can be processed simultaneously. In other words, a multiplexed training stage is required to estimate the channels of all N antennas [12]. Specifically, the minimum number of training symbols per coherence interval required to estimate the complete channel is given by

$$\eta_{tr} = K \times \left\lceil \frac{N}{M} \right\rceil, \quad (3)$$

where $\lceil \cdot \rceil$ rounds to the highest closer integer. The resultant number of time-frequency symbols dedicated to downlink data transmission per coherence block is given by¹

$$\eta_{dl} = \eta_{coh} - \eta_{ul} - \eta_{tr} = \eta_{coh} - \eta_{ul} - \eta_{tr}, \quad (4)$$

¹For simplicity, we ignore the guard intervals between the uplink, downlink and pilot transmission stages found in realistic TDD systems.

where η_{ul} refers to the number of uplink data symbols in each coherence block, as illustrated in Fig. 1.

2) *Power-Based AS for Reduced-RF Systems ($M < N$):* An elegant solution to the CSI acquisition problem in AS systems consists in adopting a selection decision hinging on the norm of the channel entries. With this purpose, let $\bar{\mathbf{h}} = [\|\mathbf{h}_1\|^2, \dots, \|\mathbf{h}_N\|^2] \in \mathbb{R}^{1 \times N}$ be a vector comprising the channel power measured per antenna element, where \mathbf{h}_i denotes the i -th column of \mathbf{H} . Let $\mathbf{S} \in \mathbb{B}^{N \times N}$ be a diagonal binary matrix that indicates whether the i -th antenna element is selected, i.e.

$$\mathbf{S}_{i,i} = \begin{cases} 1, & \text{if } i \in \mathcal{M}, \\ 0, & \text{otherwise.} \end{cases} \quad (5)$$

For the case of power-based AS, \mathbf{S} can be obtained by solving

$$\begin{aligned} \mathcal{P}_1 : \quad & \underset{\mathbf{S}}{\text{maximize}} \quad \bar{\mathbf{h}}\mathbf{S} \\ & \text{subject to} \quad \sum_{i=1}^N \mathbf{S}_{i,i} = M, \\ & \quad \quad \quad \mathbf{S}_{i,i} \in \{0, 1\}, \quad \forall i \in \{1, \dots, N\}. \end{aligned} \quad (6)$$

This strategy is commonly referred to as power-based or norm-based AS. The solution to the power-based AS of \mathcal{P}_1 is straightforward for the case of fully-flexible architectures, i.e. [11]

$$\tilde{\mathbf{S}}^* = \max_M \|\mathbf{h}_i\|^2, \quad (7)$$

where $\max_M(\cdot)$ selects the largest M entries. Power-based AS generally provides a sub-optimal decision for maximizing the sum capacity of MU-MIMO systems, since the latter does not only depend on the received signal power but also on the orthogonality between the channels of different users [10]. However, power-based AS is also capable of reducing the amount of time resources spent for CSI acquisition in systems where RF power meters instead of full RF chains are attached to each antenna port [12]. This is because a) the channel power information can be acquired from the prior uplink stage and b) this information can be subsequently employed for AS as per (6). The above entails that power-based AS only requires a minimum of $\eta_{tr} = K$ training pilots [6].

C. Sources of Losses in the RF Switching Matrices of AS

The design of the RF switching matrix in AS plays a fundamental role in the overall system performance [10]. Among the multiple technical aspects that should be considered from a system-level design perspective, the most relevant ones are:

- *Insertion losses.* Insertion losses account for the signal power losses introduced by RF switching matrices when the input power is not fully delivered to the output ports [34]. These losses generally grow with the number of input and output ports [27], [28]. Therefore, this is a critical parameter in massive MIMO, due to the large number of RF chains and antennas implemented [3], [22].
- *Coupling between ports.* The coupling of the switching matrix determines the fraction of the signals that appear at a specific port, but were intended for other ports.

This parameter depends on the network of connections inside the switching matrix and the power leakage of the internal switching devices (e.g., FET transistors or electromechanical switches) [35].

- *Transfer function balance.* The transfer function of each input-output combination between RF chain and antenna element should be ideally identical to ensure that the baseband model (1) accurately characterizes the system's operation [10].

From the above three sources of losses, here we focus on the insertion losses. This is because a) transfer function imbalances can be compensated by means of an initial system calibration [10] and b) coupling between internal switching ports can be in the order of -20 to -30 dB, hence effectively making unintentional power transfers between nearby ports negligible [36]. Instead, the insertion losses introduced by switching matrices with a large number of input and output ports can be in the order of 2-3 dB [28], hence dominating the overall performance loss. For this reason, Sec. III explores a number of implementations of conventional fully-flexible switching matrices, with emphasis on the number of switching components required and the associated insertion losses.

III. FULLY-FLEXIBLE SWITCHING FOR ANTENNA SELECTION

RF switching matrices in conventional AS have two essential requirements: a) connecting each RF chain to the antenna ports (full flexibility) and b) allowing bidirectional switching for uplink-downlink operation [10], [27]. An illustrative example of a switching matrix is shown in Fig. 2(a), where the block diagram of a 4×8 architecture can be observed. This figure shows that a large switching matrix is comprised of two main switching stages (represented by the dashed boxes in the figure) with multiple switches of smaller size: one stage at the RF-chain ports, referred to as RF-chain switching stage in the sequel, and a subsequent stage at the antenna ports, referred to as antenna switching stage.

Each of the above two switching stages is comprised by a number of switches with a smaller number of ports, which will be referred to as *basic switches* in the following. Indeed, the RF-chain switching stage of Fig. 2(a) illustrates that several of these basic switches can be concatenated to produce the desired number of ports [28]. The basic switches considered in this paper conventionally follow the nomenclature SPXT (single-pole X-throw) which refers to the number of separate ports with independent signals that the basic switch can control (poles) and the number of different signal paths that the switch allows for each pole (throws) [36], [37]. For instance, a SP3T switch is capable of routing one signal from or towards three different ports (throws). Without loss of generality, we consider the SP2T-SP4T basic switches with their insertion loss detailed in Table I for the illustrative architectures shown in this paper [36].

Conventional AS systems consider fully-flexible switching matrices that allow any possible combination of M antennas to be simultaneously selected. However, a number of implementations for the design of switching matrices with varying complexity and insertion loss can be implemented. Importantly,

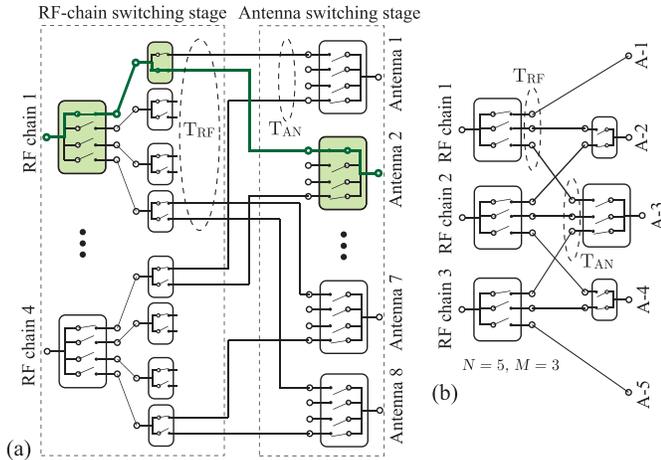


Fig. 2. Block diagrams of fully-flexible architectures: (a) a conventional 4×8 switching matrix with each input port connected to every output port (full flexibility with full connectivity) and (b) a switching matrix minimizing the number of internal connections (full flexibility with minimum connectivity).

TABLE I
BASIC SPXT SWITCHES

Switch type	Model [36]	Insertion loss
SP2T	PE42422	$L_2 = 0.25$ dB
SP3T	PE42430	$L_3 = 0.45$ dB
SP4T	PE42440	$L_4 = 0.45$ dB

different architectures may result in RF-chain and antenna switching stages with different number of ports (throws). An accurate characterization of the maximum number of throws per switching stage is crucial, since they determine overall insertion loss of the critical signal path, i.e. the signal path with largest power losses.

With the above purpose, let us define T_{RF} and T_{AN} as the maximum number of throws per RF chain (T_{RF}) in the RF-chain switching stage or per antenna (T_{AN}) in the antenna switching stage, as represented in Fig. 2(a). Moreover, let \mathcal{T} be the set with elements in decreasing order given by the number of throws in the basic switches, i.e., $\mathcal{T} = \{4, 3, 2\}$ for the basic switches considered in Table I. \mathcal{T}_j corresponds to the j -th entry in \mathcal{T} and the cardinality of \mathcal{T} is $|\mathcal{T}| \triangleq N_s$. Intuitively, N_s refers to the number of different basic switches considered, i.e. $N_s = 3$ for the basic switches of Table I. The total insertion loss measured in dB of the critical signal path L for a given switching architecture can be computed as

$$L = \sum_{j=1}^{N_s} (S_{\mathcal{T}_j}^{RF} + S_{\mathcal{T}_j}^{AN}) \times L_{\mathcal{T}_j} = \sum_{j=1}^{N_s} S_{\mathcal{T}_j} \times L_{\mathcal{T}_j}, \quad (8)$$

where $S_{\mathcal{T}_j}^{RF}$ and $S_{\mathcal{T}_j}^{AN}$ represent the number of consecutive basic switches with \mathcal{T}_j throws that the signals cross in the RF-chain and in the antenna switching stages, respectively, and $L_{\mathcal{T}_j}$ denotes the insertion loss in dB introduced by a basic switch with \mathcal{T}_j throws. For instance, we have $L_2 = 0.25$ dB, $L_3 = 0.45$ dB and $L_4 = 0.45$ dB for the basic switches considered in Table I. Moreover, $S_{\mathcal{T}_j} \triangleq S_{\mathcal{T}_j}^{RF} + S_{\mathcal{T}_j}^{AN}$ refers to the total number of switches with \mathcal{T}_j throws in the critical signal path of the overall switching matrix.

The number of basic switches with \mathcal{T}_j throws crossed by the transmit signals in the RF-chain and antenna switching stages can be iteratively computed as²

$$S_{\mathcal{T}_j}^{(RF,AN)} = \text{fact} \left(Q_{\mathcal{T}_j}^{(RF,AN)}, \mathcal{T}_j \right), \quad j \in \{1, \dots, N_s\}, \quad (9)$$

where $\text{fact}(a, b)$ denotes the number of times b appears in the integer factorization of a and $Q_{\mathcal{T}_j}^{(RF,AN)}$ is given by

$$Q_{\mathcal{T}_j}^{(RF,AN)} = \begin{cases} T_{(RF,AN)}, & \text{if } j = 1, \\ Q_{\mathcal{T}_{j-1}}^{(RF,AN)} / \max \left(\mathcal{T}_{j-1}^{(S_{\mathcal{T}_{j-1}}^{(RF,AN)})}, 1 \right), & \text{otherwise.} \end{cases} \quad (10)$$

In plain words, $Q_{\mathcal{T}_j}^{(RF,AN)}$ indicates the maximum number of throws per RF-chain or per antenna that should be implemented by concatenating basic switches with \mathcal{T}_j throws. Moreover, $\mathcal{T}_j^{S_{\mathcal{T}_j}}$ represents the number of throws obtained by $S_{\mathcal{T}_j}$ subsequent switching stages comprised of basic switches with \mathcal{T}_j throws, as described in the illustrative example of Table II for the $N = 128$, $M = 76$ case.

Considering the above, in this work we explore three implementations for the design of fully-flexible switching matrices:

- 1) *Architecture 1. Conventional fully-flexible architecture with full connectivity.* This architecture is illustrated in Fig. 2(a), where it can be seen that each RF chain is connected to every antenna port. In this particular case

$$T_{RF} = N, \quad \text{and} \quad T_{AN} = M. \quad (11)$$

- 2) *Architecture 2. Fully-flexible architecture with minimum connectivity.* This architecture minimizes the maximum number of ports at the RF-chain and antenna switching stages. The block diagram of this architecture is shown in Fig. 2(b) for an illustrative 3×5 RF switching matrix. Here, it can be seen that there are additional constraints regarding the connectivity of each antenna. For instance, Fig. 2(b) shows that the first RF chain does not connect to antenna ports A-4 and A-5. In spite of this, a full flexibility is guaranteed provided that

$$T_{RF} = N - M + 1, \quad \text{and} \quad T_{AN} = \min(M, T_{RF}). \quad (12)$$

Overall, the fully-flexible with minimum connectivity architecture selects the basic switches such that T_{RF} and T_{AN} are minimized, while ensuring that any combination of antennas can be simultaneously activated. Altogether, this architecture aims to minimize the number of connections to simplify the hardware design.

- 3) *Architecture 3. Fully-flexible architecture with minimum losses.* The fully-flexible with minimum connectivity architecture does not guarantee that the insertion losses are minimized, since designing networks with larger T_{RF} or T_{AN} might actually reduce the total insertion loss. This counter-intuitive behaviour is illustrated in Table II, where it can be observed that minimizing the T_{RF} and T_{AN} as per the fully-flexible with minimum connectivity architecture does not minimize the total insertion loss.

²For simplicity, it has been considered that T_{RF} and T_{AN} can be factorized into the integers contained in \mathcal{T} . Otherwise, T_{RF} and T_{AN} represent the closest greater integer that can be factorized.

TABLE II
INSERTION LOSSES INTRODUCED BY DIFFERENT SWITCHING ARCHITECTURES IN AN AS SYSTEM WITH $N = 128$ AND $M = 76$

Parameter	Full flexibility with full connectivity	Full flexibility with minimum connectivity	Full flexibility with minimum insertion losses	Partial connectivity
T_{RF}	128	54	64	2
T_{AN}	81	54	64	2
$Q_{\{4,3,2\}}^{RF}$ as per (10)	$Q_{\{4,3,2\}}^{RF} = \{128, 2, 2\}$	$Q_{\{4,3,2\}}^{RF} = \{54, 54, 2\}$	$Q_{\{4,3,2\}}^{RF} = \{64, 0, 0\}$	$Q_{\{4,3,2\}}^{RF} = \{2, 2, 2\}$
{SP4T, SP3T, SP2T} basic switches required per RF chain ($S_{\{4,3,2\}}^{RF}$)	$S_{\{4,3,2\}}^{RF} = \{3, 0, 1\}$	$S_{\{4,3,2\}}^{RF} = \{0, 3, 1\}$	$S_{\{4,3,2\}}^{RF} = \{3, 0, 0\}$	$S_{\{4,3,2\}}^{RF} = \{0, 0, 1\}$
$Q_{\{4,3,2\}}^{AN}$ as per (10)	$Q_{\{4,3,2\}}^{AN} = \{81, 81, 0\}$	$Q_{\{4,3,2\}}^{AN} = \{54, 54, 2\}$	$Q_{\{4,3,2\}}^{AN} = \{64, 0, 0\}$	$Q_{\{4,3,2\}}^{AN} = \{2, 2, 2\}$
{SP4T, SP3T, SP2T} basic switches required per antenna ($S_{\{4,3,2\}}^{AN}$)	$S_{\{4,3,2\}}^{AN} = \{0, 4, 0\}$	$S_{\{4,3,2\}}^{AN} = \{0, 3, 1\}$	$S_{\{4,3,2\}}^{AN} = \{3, 0, 0\}$	$S_{\{4,3,2\}}^{AN} = \{0, 0, 1\}$
Total number of {SP4T, SP3T, SP2T} basic switches ($S_{\{4,3,2\}}$)	$S_{\{4,3,2\}} = \{3, 4, 1\}$	$S_{\{4,3,2\}} = \{0, 6, 2\}$	$S_{\{4,3,2\}} = \{6, 0, 0\}$	$S_{\{4,3,2\}} = \{0, 0, 2\}$
Total insertion loss in dB (L) as per (8), where L_{T_j} are given by Table I	$L = 3 \times 0.45 + 4 \times 0.45 + 0.25 = 3.4$ dB	$L = 6 \times 0.45 + 2 \times 0.25 = 3.2$ dB	$L = 6 \times 0.45 = 2.7$ dB	$L = 2 \times 0.25 = 0.5$ dB

This is because the insertion losses introduced by the basic switches vary depending on their number of ports as per Table I, an effect particularly noticeable for large T_{RF} and T_{AN} . In contrast, the fully-flexible architecture with minimum losses selects the T_{RF} and T_{AN} that minimize the insertion loss by relaxing (12) into

$$T_{RF} \geq N - M + 1, \quad \text{and} \quad T_{AN} \geq \min(M, T_{RF}). \quad (13)$$

At this point we note that while the different design criteria considered above guarantee full flexibility, their insertion loss can still be as significant as 2-3 dB for massive MIMO systems, as demonstrated in Table II and shown in Sec. VII. For this reason, in the following we concentrate on reducing the complexity and losses of RF switching matrices at the expense of reducing their flexibility.

IV. PARTIALLY-CONNECTED SWITCHING ARCHITECTURES AND RESULTING AS CONSTRAINTS

The significant insertion loss and complexity of fully-flexible switching matrices in massive MIMO motivate the design of alternative low-complexity switching architectures [22]. In this section we consider a partially-connected switching architecture with arbitrary N and M for alleviating the above-mentioned concerns. Specifically, the partially-connected architecture is designed to reduce the number of internal connections and basic switches, i.e.

$$T_{RF} = \left\lceil \frac{N}{M} \right\rceil, \quad \text{and} \quad T_{AN} = \begin{cases} 1, & \text{if } \lfloor \frac{N}{M} \rfloor \geq 2, \\ 2, & \text{otherwise,} \end{cases} \quad (14)$$

where the maximum number of throws per RF chain in the RF-chain switching stage, T_{RF} , is defined to guarantee the essential constraint of connecting every antenna to at least one RF chain. Moreover, (14) shows that there are two specific cases depending on the ratio $\frac{N}{M}$:

- 1) *Case for* $\lfloor \frac{N}{M} \rfloor \geq 2$. Partially-connected switching schemes satisfying this condition are those where each antenna is connected to a single RF chain ($T_{AN} = 1$). We note that this architecture comprehends the particular

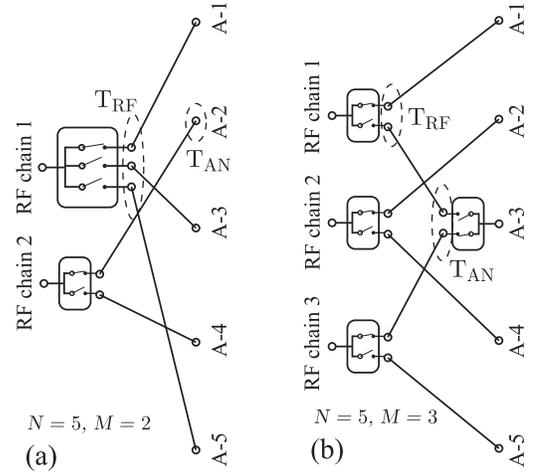


Fig. 3. Illustrative block diagrams for the partially-connected switching architectures of (a) $\lfloor \frac{N}{M} \rfloor \geq 2$ and (b) $\lfloor \frac{N}{M} \rfloor < 2$.

case $N/M = 2$ considered in [22], where only SP2T switches are required to ensure antenna connectivity. An illustrative example is shown in Fig. 3(a), where $N = 5$ and $M = 2$. In this architecture, it can be observed that one SP3T must be implemented at the RF-chain switching stage for guaranteeing that every antenna is connected to one RF chain.

- 2) *Case for* $\lfloor \frac{N}{M} \rfloor < 2$. In these architectures each antenna might be connected to more than one RF chain ($T_{AN} = 2$). Accordingly, employing basic switches SP2T suffices to guarantee connectivity for all N antennas. A specific switching architecture satisfying $\lfloor N/M \rfloor < 2$ is shown in Fig. 3(b) for $N = 5$ and $M = 3$. This figure shows that the SP2T switches required at the antenna switching stage introduce additional insertion losses with respect to the case $\lfloor N/M \rfloor \geq 2$.

A. Optimization Constraints for AS With Instantaneous CSI

Overall, the above partially-connected architectures impose a limited flexibility in the AS procedure, since the simultaneous selection of certain antenna combinations is not imple-

mentable. In this context, the specific restrictions introduced by the partial connectivity should be considered when defining the antenna selection problem [22]. With this purpose, let $\overline{\mathcal{N}} = \{1, \dots, N\}$ and $\overline{\mathcal{M}} = \{1, \dots, M\}$ be sets indexing the antennas and the RF chains, respectively. We define the antenna subsets $\mathcal{N}^i, i \in \{1, \dots, S_{\text{cons}}\}$ as disjoint sets of antennas sharing at least one common RF chain, i.e. $\mathcal{N}^1 \cup \mathcal{N}^2 \cup \dots \cup \mathcal{N}^{S_{\text{cons}}} = \overline{\mathcal{N}}$ and $\mathcal{N}^i \cap \mathcal{N}^j = \emptyset, \forall i, j \in \{1, \dots, S_{\text{cons}}\}$. Here, \emptyset denotes the empty set and S_{cons} denotes the number of constraints detailed below. Similarly, $\mathcal{M}^i \subset \overline{\mathcal{M}}, i \in \{1, \dots, S_{\text{cons}}\}$ represent disjoint sets of RF chains sharing at least one common antenna. For the illustrative example of Fig. 3(b), the sets \mathcal{M}^i are defined as $\mathcal{M}^1 = \{1, 3\}$, since these RF chains inter-connect antennas $\mathcal{N}^1 = \{1, 3, 5\}$, and $\mathcal{M}^2 = \{2\}$, which inter-connects antennas $\mathcal{N}^2 = \{2, 4\}$. Intuitively, the cardinality of the antenna groups sharing at least one RF chain (S_{cons}) also represents the number of constraints required to account for partial connectivity, which is given by

$$S_{\text{cons}} = M - N_{\text{ov}}, \quad (15)$$

where N_{ov} represents the number of antennas with the possibility of connecting to more than one RF chain. This is because the partial connectivity architecture imposes an additional constraint for every subset of antennas $\mathcal{N}^i \subset \overline{\mathcal{N}}, i \in \{1, \dots, S_{\text{cons}}\}$, interconnected with their corresponding subset of RF chains $\mathcal{M}^i \subset \overline{\mathcal{M}}, i \in \{1, \dots, S_{\text{cons}}\}$. For instance, Fig. 3(b) shows that only two out of antennas $\mathcal{N}^1 = \{1, 3, 5\}$ can be simultaneously active, since these antennas are only connected to two RF chains ($|\mathcal{M}^1| = 2$).

Without loss of generality, we consider in the following that each RF chain is connected to antennas physically separated as shown in Fig. 3(a) and Fig. 3(b).³ Based on the above, let us define

$$N_{\text{dist}} = \begin{cases} M, & \text{if } \lfloor N/M \rfloor \geq 2, \\ N - M, & \text{otherwise.} \end{cases} \quad (16)$$

In plain words, N_{dist} represents the distance between antennas inter-connected to a given RF chain. Similarly, the distance between the RF chains connected to a given subset of antennas M_{dist} is given by

$$M_{\text{dist}} = \begin{cases} 1, & \text{if } \lfloor N/M \rfloor \geq 2, \\ N - M, & \text{otherwise.} \end{cases} \quad (17)$$

For example, Fig. 3(b) shows that RF chains $\mathcal{M}^1 = \{1, 3\}$, which inter-connect antennas $\mathcal{N}^1 = \{1, 3, 5\}$, are separated by $M_{\text{dist}} = N - M = 2$ to minimize the length of the longest wired connection. Accordingly, we can express \mathcal{N}^i and \mathcal{M}^i for the non-adjacent antenna connectivity considered in this paper as

$$\mathcal{N}^i = \left\{ i, i + N_{\text{dist}}, \dots, i + \left(\left\lceil \frac{N-i+1}{N_{\text{dist}}} \right\rceil - 1 \right) N_{\text{dist}} \right\}, \quad (18)$$

where each RF chain connects to antennas with indices separated by N_{dist} and

$$\mathcal{M}^i = \left\{ i, i + M_{\text{dist}}, \dots, i + \left(\left\lceil \frac{M-i+1}{M_{\text{dist}}} \right\rceil - 1 \right) M_{\text{dist}} \right\}, \quad (19)$$

³This consideration is motivated by the results obtained in [22] for real propagation environments, where it was shown that inter-connecting a given RF chain to non-adjacent antennas provides a better performance than the connection to adjacent antennas due to their enhanced spatial diversity.

where each antenna connects to RF chains with indices separated by M_{dist} . Considering the above, the AS optimization problem for general partially-connected architectures can be formulated as

$$\mathcal{P}_2: \quad \underset{\mathbf{S}, \mathbf{P}}{\text{maximize}} \quad \log_2 \det \left(\mathbf{I}_K + \rho \mathbf{P} \mathbf{H} \mathbf{S} \mathbf{H}^H \right) \quad (20a)$$

$$\text{subject to} \quad \sum_{i=1}^N \mathbf{S}_{i,i} = M, \quad (20b)$$

$$0 \leq \mathbf{S}_{i,i} \leq 1, \quad \forall i \in \{1, \dots, N\}, \quad (20c)$$

$$\sum_{j \in \mathcal{N}^i} \mathbf{S}_{j,j} = |\mathcal{M}^i|, \quad \forall i \in \{1, \dots, S_{\text{cons}}\}, \quad (20d)$$

In resemblance with [21], [22], [32], \mathcal{P}_2 is solved by first optimizing over \mathbf{S} , while setting $\mathbf{P} = \mathbf{I}_K$. Note that the resultant convex optimization problem is produced by relaxing binary constraints, so the solution $\tilde{\mathbf{S}}^*$ can be obtained by choosing the M largest entries of \mathbf{S} compliant with the partial connectivity limitations. Subsequently, the user power allocation matrix $\tilde{\mathbf{P}}^*$ is computed by maximizing (2) with \mathcal{M} fixed as per (5). This is also a convex optimization problem with a waterfilling-type solution [38].

B. Optimization Constraints for AS With Power-Based CSI

The specific RF switching architecture selected also influences the procedure for performing AS based on the channel power at each of the antenna ports. Indeed, the optimization problem \mathcal{P}_1 must incorporate additional constraints due to the partial connectivity. As a result, the optimization problem for power-based AS under limited connectivity can be expressed as

$$\mathcal{P}_3: \quad \underset{\mathbf{S}}{\text{maximize}} \quad \bar{\mathbf{h}} \mathbf{S}, \quad (21a)$$

$$\text{subject to} \quad \sum_{i=1}^N \mathbf{S}_{i,i} = M, \quad (21b)$$

$$\mathbf{S}_{i,i} \in \{0, 1\}, \quad \forall i \in \{1, \dots, N\}, \quad (21c)$$

$$\sum_{j \in \mathcal{N}^i} \mathbf{S}_{j,j} = |\mathcal{M}^i|, \quad \forall i \in \{1, \dots, S_{\text{cons}}\}, \quad (21d)$$

where the sets \mathcal{N}^i and \mathcal{M}^i are defined in (18) and (19), respectively. Intuitively, the solution of \mathcal{P}_3 can be obtained by selecting the $|\mathcal{M}^i|$ largest entries of $\bar{\mathbf{h}}_{\mathcal{N}^i}, \forall i \in \{1, \dots, S_{\text{cons}}\}$.

C. Implications of Reducing Connectivity

The constraints introduced to account for partial connectivity have a number of practical system-level implications that we detail as follows:

- *Insertion losses.* The insertion losses introduced will be smaller in the design with partial switching connectivity due to the smaller number of basic switches required to implement the switching matrix. For instance, in the example of Table II, the insertion losses are reduced from $L \approx 3$ dB to $L = 0.5$ dB.

- *Power-based AS.* The need of solving \mathcal{P}_3 entails that the overall signal processing complexity of the power-based AS under restricted RF switching connectivity is modified w.r.t. its fully-flexible counterpart. This is because of the reduced length of the sub-arrays from from which the largest elements are selected (from N to $\max(|\mathcal{M}^i|) \forall i \in \{1, \dots, S_{\text{cons}}\}$).
- *System performance.* Limiting the connectivity affects the number of possible antenna combinations that can be selected. Due to its importance, this aspect is studied in Sec. V, where the ergodic sum capacity loss w.r.t. fully-flexible architectures is characterized for the case of power-based AS.
- *Baseband signal processing.* The digital signal processor (DSP) must account for the reduced connectivity in order to perform the precoding / detection operations. This is because the order of the antenna channels might be different from the conventional order at the antenna ports. Let us consider the architecture shown in Fig. 3(a) and assume that antennas $\mathcal{N}^2 = 2$ and $\mathcal{N}^1 = 5$ are those that maximize the sum capacity. For adequate symbol-to-antenna mapping, the DSP should be aware that the first RF chain will be connected to antenna port 5 whereas the second RF chain will be connected to antenna port 2. For instance, the precoding/detection weight applied to the first RF chain should be that of antenna 5 to guarantee a correct system operation.
- *Analog hardware complexity.* The employment of a reduced number of basic switches and connections can reduce cross-coupling between hardware components that are physically close, the time required for calibrating the input-output transfer function and the overall economic cost for implementing the switching matrix.

D. Practical Hardware Implementation for Power-Based AS in Massive MIMO

The solution advocated in [12] for reducing the CSI acquisition time in power-based AS is based on implementing analog power estimators at each antenna as shown in Fig. 4(a), i.e. $N_{\text{PM}} = N$, where N_{PM} refers to the number of power meters. Note that here we consider that the speed and resolution requirements of the analog-to-digital converters (ADC) required in the parallel power-meter chains can be relaxed because power-based AS solely relies on the relative differences between the received signals from different antennas. Nevertheless, additional data ports are required at the digital signal processor (DSP) to acquire this data.

The excessive number of antennas implemented in massive MIMO systems also motivates the implementation of a considerable number of RF chains, even when AS is implemented [21]. Since each RF chain captures the channel power information during the uplink data stage, this entails that the solution illustrated in Fig. 4(a) acquires a significant amount of redundant data. This redundancy is eliminated in the architecture considered in Fig. 4(b), where only $N_{\text{PM}} = N - M$ power meters are required. The particular example shown in Fig. 4(b) corresponds to an architecture with $N = 2M$. When compared with the scheme of Fig. 4(a), it can be seen that,

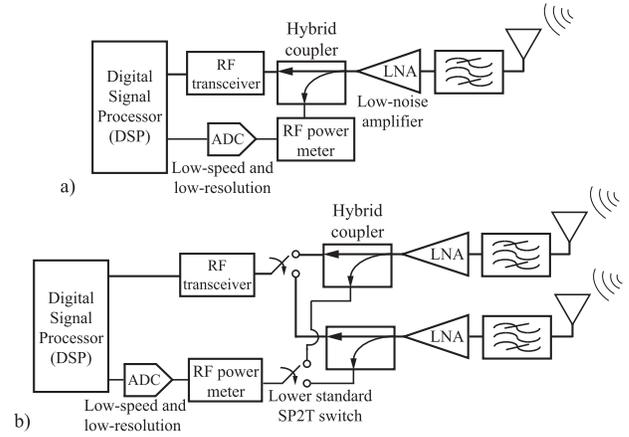


Fig. 4. Block diagram of different hardware implementations for measuring channel power. (a) One parallel power meter chain per antenna and (b) limited number of power meter chains with additional selection switches.

while additional RF switches are required, the number of parallel power-meter chains and data ports at the DSP can be substantially reduced.

V. PERFORMANCE ANALYSIS: DEGRADATION DUE TO LIMITED SWITCHING CONNECTIVITY

The partial connectivity architecture presented in Sec. IV reduces the insertion loss introduced by the switching matrix at the expense of limiting the number of number of antenna combinations that can be simultaneously selected, which entails a loss in performance. In this section, we characterize this loss by following the intuitive approach adopted in [39] for power-based AS. The reasons for focusing on the simpler power-based AS are twofold: a) its reduced CSI acquisition time as detailed in Sec. II-B by use of RF power meters, and b) the negligible performance loss introduced by this scheme when compared with the selection based on instantaneous CSI for the channels considered in this paper.

A. Ergodic Capacity Approximation for Fully-Flexible AS

The analysis developed in [39] essentially relies on approximating $\mathbf{H}_{[M]}$ in (2) by a matrix $\mathbf{V} \in \mathbb{C}^{K \times M}$ with entries following an identical distribution but with each column having different variances, i.e.

$$\mathbf{H}_{[M]} \approx \mathbf{V} = \mathbf{G}\Theta, \quad (22)$$

where $\mathbf{G} \in \mathbb{C}^{K \times M}$ is a matrix whose entries follow the same distribution of those from \mathbf{H} and $\Theta \in \mathbb{R}^{M \times M}$ is a diagonal matrix whose definition is considered in the following. Specifically, let $B_c = \sum_i |h_{c,i}|^2 = \|\mathbf{h}_c\|^2$ denote the norm of the c -th column of \mathbf{H} and define $B_{t:N}$ as the t -th smallest column norm of \mathbf{H} as per $\mathcal{W} = \{B_{1:N} < B_{2:N} < \dots < B_{N:N}\}$. Then, the diagonal entries of Θ are given by [39]

$$\theta_{i,i} = \mathbb{E}[B_{t_i:N}] / \sqrt{K}, \quad t_i \in \{1, \dots, N\}. \quad (23)$$

The definition of the indices t_i is straightforward and deterministic for the case of fully-flexible switching networks, since the

power-based AS will always select the antennas corresponding to the largest column norms of \mathbf{H} , i.e.

$$\Theta = \text{diag} \left(\sqrt{B_{N:N}}, \sqrt{B_{N-1:N}}, \dots, \sqrt{B_{N-M+1:N}} \right) / \sqrt{K}. \quad (24)$$

A simpler approximation of the resultant channel in (22) can be obtained by averaging the power scaling factors of the selected ordered statistics in (23), which for fully-flexible switching networks yields

$$\tilde{P}_{\text{FF}} = \frac{1}{KM} \sum_{i=1}^M \mathbb{E}_{\mathbf{H}} [B_{N-i+1:N}], \quad (25)$$

where $\mathbb{E}_{\mathbf{H}}$ denotes that the expectation is taken over the small-scale fading parameters of the random channel \mathbf{H} . In contrast with [39], in this work we rely on an approximation of the first moment of the t -th ordered random variable $B_{t:N}$. This is because the expressions employed in [39] are not adequate for systems with a large number of antennas due to the excessive complexity involved in their calculation. Specifically, we approximate the first moment of the t -th ordered random variable $B_{t:N}$ in uncorrelated Rayleigh flat-fading channels as [40], [41]

$$\mathbb{E}_{\mathbf{H}} [B_{t:N}] \approx \frac{c}{2} \frac{N! ((K-1)!)^{-1}}{(t-1)! (N-t)!} \int_{-1}^1 f(x) (x+1) dx, \quad (26)$$

where $f(x)$ is given by

$$f(x) = \left(1 - \sum_{j=0}^{K-1} \frac{e^{-y} y^j}{j!} \right)^{k-1} \left(\sum_{j=0}^{K-1} \frac{e^{-y} y^j}{j!} \right)^{N-k} e^{-y} y^K. \quad (27)$$

In the above expressions, $y \triangleq c(x+1)^2/4$ and c is a large constant ($c \approx 100$ provides sufficient accuracy in the approximation [41]). The result of the definite integral in (26) can be easily computed via Gauss-Jacobi quadrature [42].

The resultant ergodic capacity with the power scaling approximation can be subsequently expressed as [39]

$$C_{\text{PS-FF}} = \mathbb{E}_{\mathbf{G}} \left[\log_2 \det \left(\mathbf{I}_K + \rho \tilde{P}_{\text{FF}} \mathbf{G} \mathbf{G}^H \right) \right], \quad (28)$$

where the expectation over \mathbf{G} has been analytically derived for a variety of correlated and uncorrelated communication channels [39].

B. Ergodic Capacity Approximation for Partially-Connected AS

While the approximation in (28) accurately characterizes the ergodic capacity attainable by fully-flexible schemes, an optimal power-based AS cannot be guaranteed under limited connectivity in general. This is because selecting the antenna combination with the largest column norms of \mathbf{H} might not be feasible due to the additional constraints imposed in the optimization problem. This entails that (24) and (25) are no longer valid for partially-connected networks. For this reason we propose to further approximate (25) by taking into

consideration the probability of selecting a given subset of the ordered set $\mathcal{W} = \{B_{1:N} < B_{2:N} < \dots < B_{N:N}\}$.

With the above purpose, let us define $\mathcal{B}^j, j \in \{1, \dots, \binom{N}{M}\}$ of cardinality $|\mathcal{B}^j| = M$ as discrete sets containing a given combination of ordered column norms of \mathbf{H} . For instance, in the architecture shown in Fig. 3(a), the relevant sets \mathcal{B}^j with non-zero probability of being selected following a power-based criterion are $\mathcal{B}^1 = \{1, 2\}$, $\mathcal{B}^2 = \{1, 3\}$ and $\mathcal{B}^3 = \{1, 4\}$. Note that the antenna with the largest power can always be selected even under partial connectivity restrictions. Moreover, let $T_j \in \{0, 1\}$ be a binary random variable that determines whether the specific combination of columns of \mathbf{H} determined by \mathcal{B}^j is selected or not. Intuitively, the limited connectivity restricts a given combination of antennas \mathcal{B}^j to be selected with a given probability. This entails that, in contrast with (25), the expectation in (23) must also be taken with respect to the discrete random variables T_j . Therefore, we propose to theoretically approximate the performance of power-based AS for partially-connected switching architectures by employing

$$\tilde{P}_{\text{PC}} = \frac{1}{KM} \mathbb{E}_{T_j} \left[\sum_{i=1}^M \mathbb{E}_{\mathbf{H}} [B_{\mathcal{B}^j:N}] \right], \quad (29)$$

where \mathcal{A}_i refers to the i -th entry of a set \mathcal{A} and the outer expectation is taken over the set of random discrete variables T_j . Closed-form expressions for the inner expectation in (29) are already available for multiple channels such as those with Rayleigh flat-fading as per (26) [39]. Instead, the outer expectation corresponds to that of a discrete random variable, which is given by

$$\begin{aligned} \tilde{P}_{\text{PC}} &= \frac{1}{KM} \sum_{j=1}^{\binom{N}{M}} \left(\sum_{i=1}^M \mathbb{E}_{\mathbf{H}} [B_{\mathcal{B}^j:N}] \right) \times P(T_j) \\ &= \sum_{j=1}^{\binom{N}{M}} (\hat{P}_j \times P(T_j)), \end{aligned} \quad (30)$$

where $P(T_j)$ refers to the probability of jointly selecting the ordered channel columns determined by \mathcal{B}^j . The probabilities associated with selecting \mathcal{B}^j can be computed depending on the specific switching connectivity and the particular channel characteristics. Specifically, by following the chain rule of probability we have

$$\begin{aligned} P(T_j) &= P \left(\bigcap_{i=1}^M \text{selecting } \mathcal{B}_i^j \right) \\ &= \prod_{i=1}^M P \left(\text{selecting } \mathcal{B}_i^j \mid \bigcap_{r=1}^{i-1} \text{selected } \mathcal{B}_r^j \right), \end{aligned} \quad (31)$$

where \cap denotes the intersection of events. To simplify the derivation of the joint probabilities in (31), in the following we concentrate on channels \mathbf{H} adopting the form [3]

$$\mathbf{H} = \mathbf{R}^{\frac{1}{2}} \mathbf{F}, \quad (32)$$

where $\mathbf{R} \in \mathbb{C}^{K \times K}$ is the deterministic channel covariance matrix and $\mathbf{F} \in \mathbb{C}^{K \times N}$ is a matrix of independent

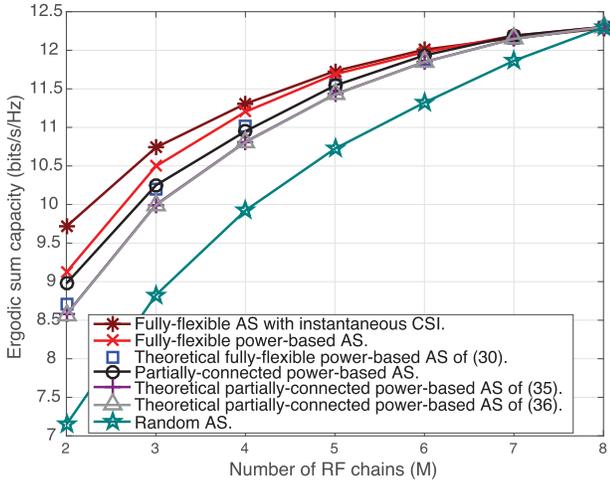


Fig. 5. Theoretical and simulated ergodic capacities (bits/s/Hz) vs. M . $N = 8$ for AS systems and $N = M$ for MU-MIMO without AS, $K = 2$ and $\rho = 10$ dB. CSI acquisition overheads and insertion losses are not considered.

and identically distributed (i.i.d.) random variables following $f_{i,j} \sim \mathcal{CN}(0, 1)$.

The above assumption entails that the probability of finding the t -th ordered statistic $B_{t:N}$, $t \in \{1, \dots, N\}$ at a given antenna is equal for all antenna elements \mathcal{A} , which makes computing the probabilities in (31) straightforward for a given switching connectivity. An example of this derivation for $N = 5$ and $M = 2$ is provided in Appendix A for completeness. Once the probabilities of selecting a given ordered channel column combination $P(T_j)$ in (30) are determined, an approximation to the analytical ergodic capacity of AS systems with partially-connected switching matrices can be expressed as

$$C_{\text{PS-PC}} \approx \mathbb{E}_{\mathbf{G}} \left[\log_2 \det \left(\mathbf{I}_K + \rho \tilde{\mathbf{P}}_{\text{PC}} \mathbf{G} \mathbf{G}^H \right) \right]. \quad (33)$$

An alternative approximation to the ergodic capacity of partially-connected switching matrices can be derived by directly computing the expectation of the capacity over both the discrete random variables T_j and \mathbf{G} . In this particular case, the approximation of the ergodic capacity is given by

$$C_{\text{PS-PC}} \approx \sum_{j=1}^{\binom{N}{M}} \mathbb{E}_{\mathbf{G}} \left[\log_2 \det \left(\mathbf{I}_K + \rho \hat{\mathbf{P}}_j \mathbf{G} \mathbf{G}^H \right) \right] \times P(T_j), \quad (34)$$

where $\hat{\mathbf{P}}_j$ is defined in (30).

The accuracy of the derived approximations can be observed in Fig. 5, which represents the ergodic capacity attained by the schemes considered in this paper against increasing M for a system with $N = 8$ for AS systems and $N = M$ for conventional MIMO without AS, $K = 2$ and $\rho = 10$ dB. Here we consider a small-scale MIMO setup for illustrative reasons since, as shown in Sec. VII, the differences between the switching architectures vanish for large N . For the results of this figure, we consider an uncorrelated Rayleigh flat-fading channel, ignore the overheads associated with CSI acquisition and assume an identical transmission power at the antenna ports, i.e., the switching insertion losses are precompensated.

In spite of this unfavourable assumption for the power-based criterion, it can be seen that it approaches the performance attained by instantaneous CSI-based AS for different values of M , an observation consistent with previous works [11], [39]. Fig. 5 also shows that the switching architecture with partial connectivity only experiences a slight performance loss w.r.t. the fully-flexible scheme, which is also coherent with the results obtained for $N = 2M$ in the real propagation channels of [22]. The slight performance loss can be explained by noting that, in general, there exists a large probability of selecting antennas with significant channel powers, even if these are not strictly the largest ones as shown in Appendix A. This result combined with those obtained in Sec. VII motivate the employment of power-based AS with partial connectivity, especially for massive MIMO where the insertion losses introduced by the switching matrices can be significant. Moreover, Fig. 5 shows that the proposed approximations in (33) and (34) capture the performance loss produced by the partial connectivity architecture.

VI. ENERGY EFFICIENCY

The enhancement of the communication's energy efficiency is a key driver for considering AS implementations in massive MIMO due to the possibility of reducing the number of RF chains simultaneously active [21], [22], [43]. This comes, however, at the expense of introducing additional insertion losses in the RF switching stage, hence posing an energy efficiency trade-off that we aim to analyze in this paper. The energy efficiency for downlink transmission can be expressed as [33], [43], [44]

$$\xi = \frac{R_{\text{sum}}}{P_{\text{PA}} + P_{\text{RF}} + M(P_{\text{ADC}} + P_{\text{DAC}} + P_{\text{int}}) + P_{\text{BB}}}, \quad (35)$$

where R_{sum} (bits/s) refers to the effective downlink sum rates after accounting for the CSI acquisition and uplink overheads, and $P_{\text{PA}} = \frac{P_t}{\kappa}$ is the power consumed by power amplifiers (PAs) with efficiency κ to produce an output signal of P_t Watts. Moreover, $P_{\text{RF}} = M P_{\text{CIR}} + P_{\text{LO}}$ represents the power consumed by the analog hardware components excluding the PAs. Here, P_{CIR} is the power consumed by the analog circuitry required in each of the RF chains, and P_{LO} denotes the power consumed by the local oscillator. P_{ADC} and P_{DAC} refer to the power consumption of dual channel (I-Q) analog-to-digital converters (ADCs) and digital-to-analog converters (DACs), respectively, whereas P_{BB} and P_{int} represent the power of the DSP baseband signal processing and its data interface per RF chain. The baseband power consumption can be decomposed as $P_{\text{BB}} = p_c C$, where p_c characterizes the power consumed per floating point operation and $C = C_{\text{corr}}^{\text{tr}} + C_{\text{data}}^{\text{dl}}$ includes the number of floating point operations for pilot correlation in the CSI acquisition stage ($C_{\text{corr}}^{\text{tr}}$) and for precoding in the downlink data transmission stage ($C_{\text{data}}^{\text{dl}}$). Additionally, the power consumed per RF chain by the DSP data interface is given by

$$P_{\text{int}} = p_{\text{int}} (2 \cdot S_{\text{ADC}} \cdot b_{\text{ADC}} + 2 \cdot S_{\text{DAC}} \cdot b_{\text{DAC}}), \quad (36)$$

where p_{int} denotes the power consumption per Gbps, b_{ADC} and b_{DAC} refer to the resolution in bits of the ADCs and DACs,

TABLE III
ENERGY EFFICIENCY PARAMETERS

Parameter	Characteristics
$P_{\text{ADC}} = 233 \text{ mW}$ [45]	$b_{\text{ADC}} = 12 \text{ bits}$, $S_{\text{ADC}} = 125 \text{ MSPS}$
$P_{\text{DAC}} = 232 \text{ mW}$ [46]	$b_{\text{DAC}} = 14 \text{ bits}$, $S_{\text{DAC}} = 125 \text{ MSPS}$
$p_{\text{int}} = 25 \text{ mW/Gbps}$ [47]	Parallel interface LVDS
$P_{\text{cir}} = 1 \text{ W}$ [33]	-
$P_{\text{LO}} = 2 \text{ W}$ [33]	-
$p_c^{-1} = 12.8 \text{ Mflops / mW}$ [33]	Eight-core C6678 DSP
$\kappa = 0.39$ [33]	-
$P_t = 46 \text{ dBm}$ [48]	LTE macro BS
Operation	Computational complexity
$C_{\text{corr}}^{\text{tr}}$ [49]	$8N_{\text{coh}}\eta_{\text{tr}}MK \text{ flops}$
$C_{\text{data}}^{\text{dl}}$ [8], [49]	$N_{\text{coh}}(4K^2M + \eta_{\text{dl}}(8KM)) \text{ flops}$

while S_{ADC} and S_{DAC} represent their sampling rate per port (I-Q) in Gbps. The values considered for the above parameters can be found in Table III, where N_{coh} denotes the number of channel coherence blocks per second and a system operating below 6 GHz has been considered.

To explicitly account for the impact of the insertion losses on the system's energy efficiency, in this work we incorporate L directly in P_t above, that is, we consider that the RF switching matrix is placed after the PAs in the transmission chain [9], [10]. This entails that PAs in systems with AS will be required to produce output signals with larger power to compensate for the insertion loss of the switching stage, i.e. $P_t^{\text{AS}} = P_t^{\text{no-AS}} \times L$ [9], [10]. While other hardware solutions such as placing the switching matrices before the PAs are certainly feasible, considering the impact of the insertion losses on the resultant system's energy efficiency also becomes more intricate. This is because precise knowledge of the PA response, which is non-linear and component-dependent, would be required to quantify the additional power required per RF chain.

VII. SIMULATION RESULTS

In this section we present numerical results for characterizing the performance of the switching architectures considered in this paper. In particular, we consider AS schemes based on both power-based and instantaneous CSI decisions under uncorrelated Rayleigh flat-fading channels. Similarly to [10], [11], [15], [18], we average over channel realizations and, unless otherwise stated, consider a single carrier transmission. Moreover, we show the sum rates attained via dirty paper coding (DPC), which attains the system's ergodic sum capacity, and for the more practical ZF precoder, computed as per [21], [22]. The results are obtained for massive MIMO systems, since the insertion losses and complexity of the fully-flexible switching networks are critical in these systems due to both the large number of RF chains and antennas deployed. For simplicity, in the following we consider that the power losses introduced by the switching matrices correspond to those of the input-output combination with largest insertion loss, i.e. the critical signal path.

The results of Fig. 6 represent the sum spectral efficiency of the schemes considered in this paper against increasing number of RF chains M in a system with $N = 128$, $K = 16$ and $\rho = 15 \text{ dB}$. This figure concentrates on the impact of having a limited channel coherence interval and the need for training in AS by a) ignoring the losses associated with the switching matrices and b) considering $\eta_{\text{coh}} = 200$. This channel coherence interval approximates a fast-varying communication channel as per standard LTE OFDM parameters, where it is considered that the channel remains approximately constant throughout 14 orthogonal subcarriers of 15 kHz and 14 OFDM symbols occupying 1 ms [5], [49]. This value could correspond to a system designed to operate at high frequencies and/or to serve users with speeds of up to 500 km/h at 2 GHz, such as LTE [5], [49]. Without loss of generality, we consider that 70% of the remaining time-frequency resources after channel estimation are allocated to downlink transmission, i.e. $\eta_{\text{dl}} = 0.7 \times [(\eta_{\text{coh}} - \eta_{\text{tr}})]$. Fig. 6 critically shows that full CSI acquisition becomes suboptimal and power-based AS becomes a more attractive approach when a short channel coherence interval is considered. This is because acquiring instantaneous CSI to perform the selection requires a larger training period, which results in a performance loss particularly significant for reduced M . Instead, power-based AS can employ the $N_{\text{PM}} = N - M$ power meters integrated in the analog stage for estimating the channel powers and, exploiting channel reciprocity, for performing the selection without requiring accurate CSI for all antennas. The results of Fig. 6 also demonstrate sudden rate variations for increasing M in the instantaneous CSI-based AS due to the need of multiplexed training as per (3).

Fig. 7 characterizes the impact of the switching insertion losses on the attainable ergodic sum capacity. This entails that ρ represents the signal power after the PAs, while the actual transmitted power is given by ρ/L , where L represents the switching insertion losses determined in Sec. III and Sec. IV. In this figure we consider $N = 64$ for AS systems and $N = M$ for MU-MIMO without AS, $K = 8$, $\rho = 20$ and ignore CSI acquisition overheads. The fully-flexible switching matrix is implemented via an architecture optimized for minimum losses as detailed in Sec. III. We note that the non-smooth behaviour of this system for large M is a consequence of the sudden decrease in the insertion losses of the switching network, a phenomenon that can be observed in Fig. 9. The results of Fig. 7 demonstrate the benefits of employing partially-connected switching matrices against fully-flexible implementations. This is due to a) their significantly reduced insertion losses and, as shown in Fig. 6, b) the high probability of selecting antenna combinations with similar or equal performance to those that are optimal. We also remark that larger performance differences between systems implementing AS and full MIMO with a smaller number of antennas are expected in practical implementations, due to the noticeable average power differences perceived by different antenna ports in real propagation environments [21].

The results of Fig. 8 illustrate the ergodic sum capacity against increasing values of the channel coherence interval η_{coh} . We assume a system with the identical simulation

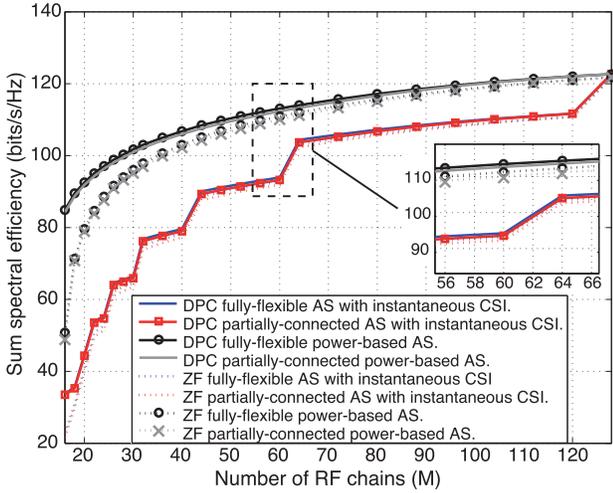


Fig. 6. Sum spectral efficiency (bits/s/Hz) vs. M for DPC and ZF. $N = 128$, $K = 16$, $\rho = 15$ dB, $\eta_{\text{coh}} = 200$ and $\eta_{\text{dl}} = 0.7 \times [(\eta_{\text{coh}} - \eta_{\text{tr}})]$. CSI acquisition overheads are considered, whereas insertion losses are ignored.

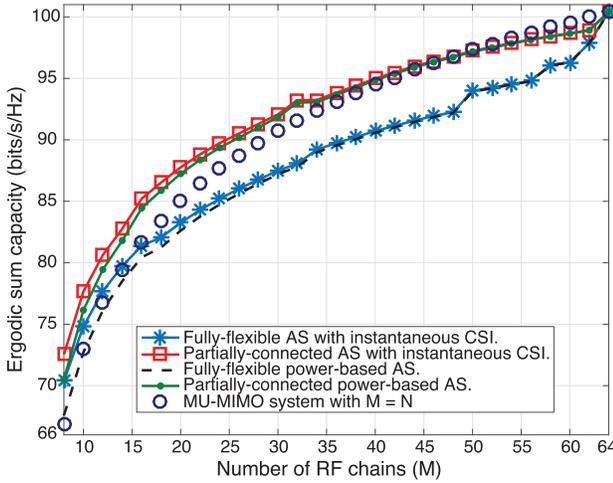


Fig. 7. Ergodic sum capacity (bits/s/Hz) vs. M for DPC with switching insertion losses. $N = 64$ for AS systems and $N = M$ for MU-MIMO without AS, $K = 8$, $\eta_{\text{dl}}/\eta_{\text{coh}} = 1$ and $\rho = 20$ dB. Insertion losses are considered and CSI acquisition overheads are not included.

parameters of Fig. 7, $M = 8$ and $\eta_{\text{dl}} = 0.7 \times [(\eta_{\text{coh}} - \eta_{\text{tr}})]$. A maximum of $\eta_{\text{coh}} = 2000$ symbols is considered, since the system behaviour is stable for larger values. This corresponds to a vehicle moving at an approximate speed of 45 km/h at a central frequency of 2 GHz. Overall, Fig. 8 illustrates the transition between fast varying and slowly varying channels and allows determining the optimal selection strategy depending on η_{coh} . For instance, the results of this figure demonstrate that power-based AS implemented via partially-connected switching is the solution that maximizes performance for a wide range of η_{coh} thanks to both their reduced insertion losses and shorter CSI acquisition interval. Moreover, it can be observed that acquiring accurate CSI might be advantageous for channels with large coherence intervals.

Fig. 9 represents the insertion losses of (8) introduced by the fully-flexible and partially-connected switching architectures considered in this paper against increasing M . We set $N = 128$ and consider the basic switches described in Table I for implementing the switching network. The

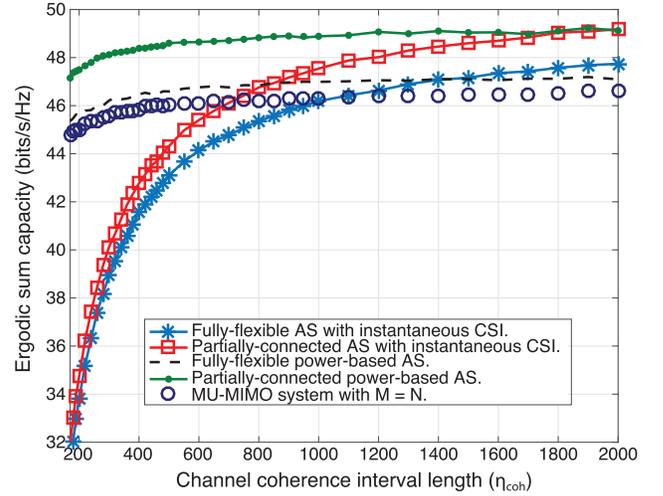


Fig. 8. Ergodic sum capacity (bits/s/Hz) vs. η_{coh} . $N = 64$ for AS systems and $N = M$ for MU-MIMO without AS, $M = K = 8$, $\rho = 20$ dB and $\eta_{\text{dl}} = 0.7 \times [(\eta_{\text{coh}} - \eta_{\text{tr}})]$. Both insertion losses and CSI acquisition overheads are considered.

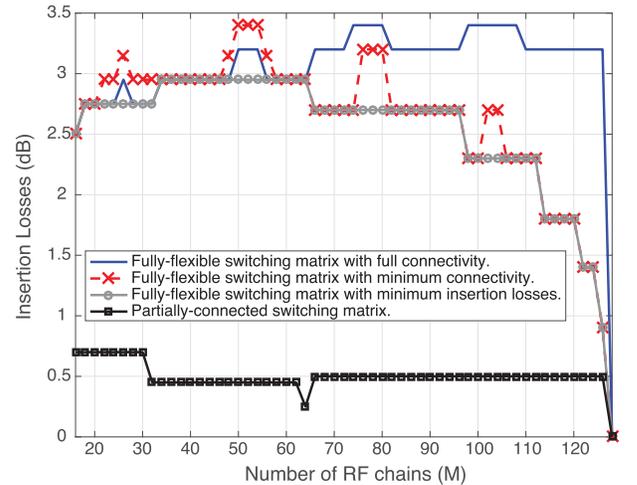


Fig. 9. Insertion losses (dB) introduced by different switching architectures vs. M . $N = 128$ and basic switching losses given in Table I.

results of Fig. 9 clearly show the benefits of considering partially-connected architectures when compared with fully-flexible schemes, which motivates their employment when simultaneously considering their performance enhancements depicted in Fig. 7 and Fig. 8. For instance, the insertion losses can be reduced by up to 2.5 dB for $M = N/2$, which is the point with minimal insertion loss for the partially-connected network and the specific case considered in [22]. This can be explained by noting that $T_{\text{RF}} = 2$ and $T_{\text{AN}} = 1$ for $M = N/2$. Instead, implementing a larger M requires additional RF switches at the output stage of the switching network as illustrated in Fig. 3(b), hence introducing additional losses in the critical signal path. However, we remark that considering a larger number of RF chains might also be required in realistic systems for satisfying specific sum rate requirements.

Fig. 9 also shows that there are non-desirable areas where the power losses can be substantially increased, if a fully-flexible architecture is preferred. Let us concentrate on understanding the behaviour these fully-flexible architectures.

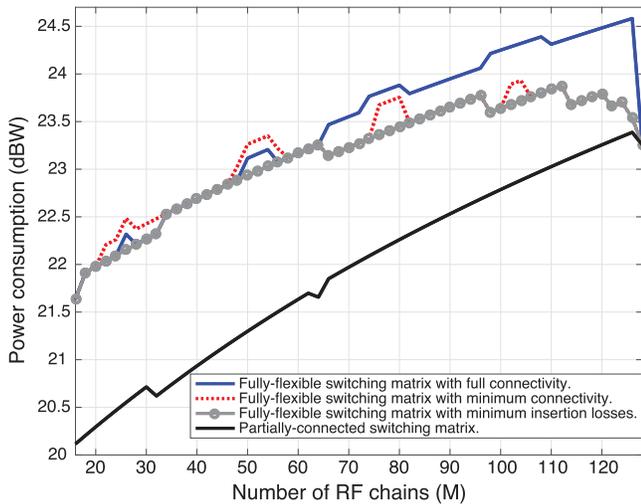


Fig. 10. Total system's power consumption vs. M . $N = 128$, $\eta_{tr} = K = 16$ and $\eta_{coh} = 200$, $\eta_{dl} = 0.7 \times [(\eta_{coh} - \eta_{tr})]$ and $N_{coh} = 1200$.

As expected, the fully-connected architecture designed to minimize the power losses introduces smaller insertion losses than those with different criteria. Moreover, it can be observed that there are points where minimizing the number of connections as per fully-flexible with minimum connectivity scheme also entails larger insertion losses than fully-flexible architectures with complete input-output connectivity. This counter-intuitive behaviour arises due to the differences in the insertion loss of the basic switches and the specific number of ports T_{RF} and T_{AN} required at the switching matrices, as analyzed in Sec. III. Overall, the results of Fig. 9 provide meaningful insights for the design of AS systems, since it can be observed that the insertion losses of different switching architectures do not follow a monotonic trend with M .

The total system power consumption P_{tot} in (35) is shown in Fig. 10 against increasing values of M . Here, we have considered the values shown in Table III and set $N = 128$, $\eta_{tr} = K = 16$ and $\eta_{coh} = 200$, $\eta_{dl} = 0.7 \times [(\eta_{coh} - \eta_{tr})]$. Moreover, $N_{coh} = 1200$ subcarriers are assumed, which corresponds to a transmission bandwidth of $B = 20$ MHz in LTE [48]. For simplicity, we also consider that the ADCs, DACs only consume power and generate data to be transmitted by the DSP data interface for the fractions of time that they are active. These correspond to CSI acquisition (η_{tr}/η_{coh}) for ADCs and to downlink data transmission (η_{dl}/η_{coh}) for PAs and DACs.

The results of Fig. 10 illustrate the importance of reducing the insertion losses in the switching stage, since substantial power savings over fully-flexible AS can be attained when employing the partially-connected architecture. Additionally, the results of this figure demonstrate that reducing the number of active RF chains can have substantial benefits in the overall power consumption. This is a direct consequence of the power consumed by a) the additional components required per each RF chain (P_{cir} and P_{DAC} , P_{ADC}), b) the additional signal processing load (P_{BB}) and c) the increased power consumption in the data interfaces (P_{int}) due to the increased amount of data generated [47].

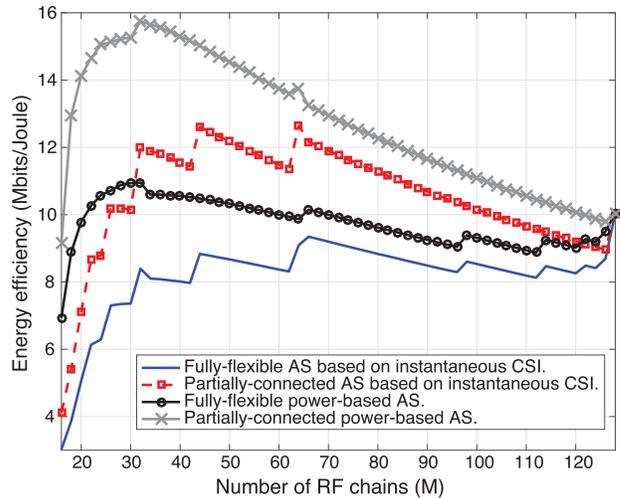


Fig. 11. Energy efficiency ζ vs. M for a ZF precoding system. $N = 128$, $\eta_{tr} = K = 16$ and $\eta_{coh} = 200$, $\eta_{dl} = 0.7 \times [(\eta_{coh} - \eta_{tr})]$, $N_{coh} = 1200$ and $\rho = 15$ dB. Insertion losses and CSI acquisition overheads are considered.

The energy efficiency ζ in (35) is shown vs. M in Fig. 11 for a ZF precoding system with the same simulation parameters of Fig. 10. Considering standard LTE values, we assume a total system bandwidth of $B = 20$ MHz, uniform power allocation throughout the entire transmission band and, without loss of generality, we concentrate on a resource block (RB) of $B_{RB} = 180$ kHz. Fig. 11 illustrates the benefits of employing both a switching network with limited connectivity and relying on power estimates for performing the AS decision. Specifically, it can be observed that the energy efficiency of the power-based partially-connected AS is maximized for $M \approx 32$, which can be explained by noting a) the significantly reduced insertion loss when compared with smaller M in Fig. 9 and b) that a large portion of the maximum attainable sum rates for $M = 128$ is already obtained for $M \approx 32$ as shown in Fig. 6. Overall, Fig. 11 demonstrates the importance of considering the insertion losses in the switching stage for energy-efficient system design.

VIII. CONCLUSION

In this paper, we analyze the impact of implementing different RF switching architectures in AS systems. The hardware features of a number of conventional fully-flexible switching designs are characterized and we show that they incur in significant insertion losses. For this reason we analyze switching matrices with partial connectivity, which are capable of significantly reducing the insertion losses of fully-flexible switching matrices. In this work we also provide analytical expressions to determine the performance loss introduced by designs with partial connectivity due to the limitation in the number of antenna subsets that can be selected. Overall, our numerical results show that partially-connected switching architectures a) are capable of providing significant improvements in the system's sum rates when insertion losses are considered and b) their combination with power-based antenna selection generally constitutes the most effective solution when CSI acquisition overheads are considered. Future work includes studying the impact of relying on the uplink data

for performing power-based AS, incorporating hardware non-idealities in the parallel power meter chains and the implementation of partially-connected switching matrices in beam switching architectures.

APPENDIX

COMPUTATION OF THE JOINT PROBABILITIES IN (31)

In this Appendix we outline the procedure for computing the probabilities $P(T_j)$ in (31) for completeness. In particular, we do this for the partially-connected architecture in Fig. 3(a) for reasons of illustration, where a scheme with $M = 2$ and $N = 5$ is considered. As detailed in Sec. V, the sets containing the combinations of the ordered column norms with non-zero probability of being selected are $\mathcal{B}^1 = \{1, 2\}$, $\mathcal{B}^2 = \{1, 3\}$ and $\mathcal{B}^3 = \{1, 4\}$. Let us start by computing $P(T_1)$, which is given by

$$P(T_1) = P\left(\text{selecting } \mathcal{B}_1^1 \cap \text{selecting } \mathcal{B}_2^1\right) = \quad (37)$$

$$\stackrel{(a)}{=} P\left(\text{selecting } \mathcal{B}_1^1\right) P\left(\text{selecting } \mathcal{B}_2^1 | \text{selected } \mathcal{B}_1^1\right) \quad (38)$$

$$\stackrel{(b)}{=} P\left(\mathcal{B}_1^1 = \{2, 4\}\right) P\left(\text{selecting } \mathcal{B}_2^1 | \mathcal{B}_1^1 = \{2, 4\}\right) \quad (39)$$

$$+ P\left(\mathcal{B}_1^1 = \{1, 3, 5\}\right) P\left(\text{selecting } \mathcal{B}_2^1 | \mathcal{B}_1^1 = \{1, 3, 5\}\right) \quad (40)$$

$$\stackrel{(c)}{=} \frac{2}{5} \times \frac{3}{4} + \frac{3}{5} \times \frac{2}{4} = \frac{3}{5}. \quad (41)$$

In the above expressions, $\stackrel{(a)}{=}$ is a direct application of (31), $\stackrel{(b)}{=}$ divides the conditional probability of selecting the second antenna with the largest channel norm depending on whether the antenna with the largest norm were antennas $\mathcal{B}_1^1 = \{1, 3, 5\}$ or antennas $\mathcal{B}_1^1 = \{2, 4\}$ and $\stackrel{(c)}{=}$ considers that the probability of finding $B_{i:N}$, $i \in \{1, \dots, N\}$ at a given antenna port is equal for all antenna elements \mathcal{N} .

A similar procedure can be employed to compute $P(T_3)$, which reads as

$$P(T_3) = P\left(\text{selecting } \mathcal{B}_1^3 \cap \text{selecting } \mathcal{B}_2^3\right) = \quad (42)$$

$$\stackrel{(a)}{=} P\left(\mathcal{B}_1^3 = \{1, 3, 5\}\right) P\left(\text{selecting } \mathcal{B}_2^3 | \mathcal{B}_1^3 = \{1, 3, 5\}\right) \quad (43)$$

$$\stackrel{(b)}{=} \frac{3}{5} P\left(\text{not selecting } B_{2:N} \cap B_{3:N} | \mathcal{B}_1^2 = \{1, 3, 5\}\right) \quad (44)$$

$$= \frac{3}{5} \times \frac{2}{4} \times \frac{1}{3} = \frac{1}{10}, \quad (45)$$

where $\stackrel{(a)}{=}$ and $\stackrel{(b)}{=}$ hold because the only possibility of selecting the antenna with the $\mathcal{B}_2^3 = 4$ -th largest norm is that the channels with the first, second and third largest norms are in antennas $\{1, 3, 5\}$. Finally, $P(T_2) = 1 - P(T_1) - P(T_3) = \frac{3}{10}$.

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